

Report 11486  
4 August 1999

**GENCORP**  
**AEROJET**

**Integrated Advanced Microwave Sounding Unit-A  
(AMSU-A)  
Performance Verification Report  
Antenna Drive Subsystem  
METSAT AMSU-A2 (PN: 1331200-2, SN: 108)**

**Contract No. NAS 5-32314  
CDRL 208**

**Submitted to:**

**National Aeronautics and Space Administration  
Goddard Space Flight Center  
Greenbelt, Maryland 20771**

**Submitted by:**

**Aerojet  
1100 West Hollyvale Street  
Azusa, California 91702**

**Aerojet**



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## AMSU-A VERIFICATION TEST REPORT

TEST ITEM:	METSAT AMSU- A2 ANTENNA DRIVE SUBSYSTEM PART OF P/N: 1331200-2 SERIAL NUMBER: 108
LEVEL OF ASSEMBLY:	SUBASSEMBLY AND COMPLETE INSTRUMENT ASSEMBLY
TYPE HARDWARE:	FLIGHT
PROCEDURE NO:	AE-26002/2E
TEST COMPLETION DATE:	22 APRIL 1999

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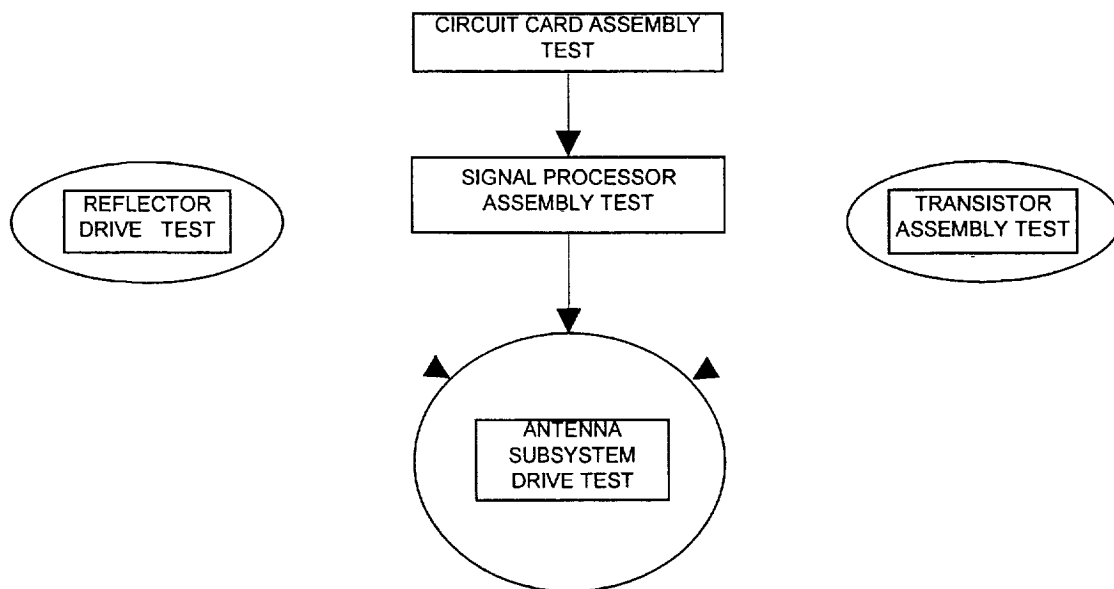
## 1.0 INTRODUCTION

The antenna drive subsystem test was performed on the METSAT AMSU-A2 S/N 108 (P/N 1331200-2) instrument. The objective of the test was to demonstrate compliance with applicable paragraphs of AMSU-A specification S-480-80 when tested using AE-26002/2E. Tests were conducted at both the subassembly and subsystem (instrument) level.

## 2.0 SUMMARY

The performance verification tests include 1) scan motion and jitter, 2) pulse load bus peak current and risetime, 3) resolver reading and position error, 4) gain and phase margin and 5) operational gain margin.

Subassembly tests are performed on the drive assembly, compensator assembly, circuit card assemblies (CCAs), signal processor and the transistor assembly. The transistor assembly was tested during the W3 cable assembly (1356946-1) test. Refer to Figure 1 for test flow.



Antenna Subsystem and Subsystem Component Test Flow  
Figure 1.

The antenna drive subsystem satisfactorily passed all tests to verify the performance requirements. There were no failures in any of the antenna drive components during subsystem testing. There were several anomalies during subassembly testing. Refer to paragraph 5.0 for a discussion of test results.

### **3.0 TEST CONFIGURATION – SUBASSEMBLIES**

Subassemblies are tested using a variety of test fixtures as required to perform the necessary tests.

***Drive Assembly*** – Prior to complete buildup of this assembly, a starting torque test is performed on the rotating part of the assembly. The test is performed at temperatures of 23, 4, and  $-10^{\circ}\text{C}$ . The tests performed on the completed assembly are 1) motor commutation, 2) resolver operation and no-load speed, 3) temperature sensor resistance and output voltage and 4) random vibration. Motor commutation and resolver operation and no-load speed are repeated after vibration.

***Compensator Assembly*** – The tests performed on this assembly are 1) motor commutation, 2) temperature sensor resistance and output voltage and 3) random vibration. Motor commutation is repeated after vibration.

***CCAs*** – All CCAs are tested prior to being incorporated into the signal processor. They are tested to verify functionality and the derived performance requirements.

***Signal Processor*** – Part of the signal processor test is associated with the antenna drive subsystem. The test includes all applicable CCAs installed in the signal processor card cage, the STE with the associated cabling to the signal processor, and a test motor and inertia wheel to simulate the antenna drive motor and reflector load. This test demonstrates that all signal processor scan drive circuitry is functioning as a subsystem prior to assembly into the instrument. During the tests, qualitative reflector position for the various scan modes is verified by visually observing an index mark on the inertia wheel.

***Transistor Assembly*** – The W3 cable is first tested on the CKT 1000 (continuity and hi-pot tester). The transistor assembly is then mated with the W3 cable, and tested using a special test fixture. The test assures that the transistors saturate when turned on, and that they turn off.

#### 4.0 TEST CONFIGURATION – SUBSYSTEM

The antenna drive subsystem tests are performed after all of the scan drive subassemblies are assembled into the instrument, and the subsystem is tested in accordance with AE-26002/2 during system integration. At the beginning of system integration testing, the instrument is first proven electrically safe by ground isolation and power distribution checks. The instrument is supplied with 28 Vdc from the STE, and the DC-DC converter is installed to supply the other required voltages to the CCAs.

The tests performed to verify performance are 1) scan motion and jitter, 2) pulse load bus peak current and risetime, 3) resolver reading and position error, 4) gain and phase margin and 5) operational gain margin. In order to verify scan motion and jitter, it is necessary to obtain real time measurement of the drive assembly shaft position. This is done by using a continuous rotation potentiometer (pot) mechanically coupled to the drive assembly shaft, and connecting a source of dc voltage across the pot. The voltage at the pot wiper then gives a voltage analog of shaft position for each revolution of the shaft.

Prior to the performance verification tests, there are five operations performed. These are described as follows:

1. An EPROM is programmed with the reflector position commands (14-bit digital words) which are calculated from the nadir position obtained on the antenna range. This PROM is one of the components on the memory board in the signal processor, and it is under microprocessor control for positioning the reflector. Reprogramming may be necessary if the measured reflector positions are not within the specified limits. (See 5.5.3).
2. After obtaining the PROM, the instrument is powered, and scan motion is qualitatively checked to conform to the pattern as shown in Appendix B1.
3. The motor (drive and compensator) current limits are set with select at test (SAT) resistors.
4. The individual steps in the scan are tailored for risetime, overshoot and jitter with SAT resistors which are part of circuits in the rate loop.
5. The mechanical resonant frequencies of the drive assembly and reflector are identified. They are then nullified by selecting the appropriate frequencies for three notch filters.

The antenna drive subsystem subassemblies designated for use in the METSAT AMSU-A2 S/N 108 instrument are shown in Table 1.

CCAs	S/N
Resolver Data Isolator	F33
Interface Converter	F25
Motor Driver 3-Hall Sensor	F03
Motor Driver 3-Hall Sensor	F02
R/D Converter/Oscillator	F11

OTHER	S/N
Antenna Drive Assembly	F07
Compensator Assembly	F05
Signal Processor	F04
Transistor Assembly (W3 Cable)	NONE

Table 1. A2 108 Subassembly S/N

## 5.0 TEST RESULTS

The test results for the subassemblies are first presented in paragraphs 5.1 through 5.4. The subsystem test results are presented in 5.5.

### 5.1 DRIVE AND COMPENSATOR ASSEMBLIES

During electrical test of the F07 drive assembly, the motor would not start (TAR 003199). Disassembly and inspection revealed that excessive bonding material resulted in bonding of the shaft and cover. The excess material was removed, and the area was cleaned. Step 20 of MAI 32 was revised to clarify the quantity and placement of bonding material. The unit then passed electrical test.

During electrical test of the F05 compensator assembly, the motor would not start (TAR 003124). Disassembly and inspection revealed that the Hall effect sensors had been broken off from the circuit board. It was determined that this occurred during assembly when the temporary spacers between the rotating assembly and stator were being removed. The number of spacers was reduced, and their placement was changed to preclude this happening again. The Hall sensor board was replaced, and the unit passed electrical test.

## **5.2 CCAs**

There were no test anomalies or failures during testing of the CCAs for this instrument. The test data sheets (TDSs) for the CCAs are presented in Appendices A1 through A4.

## **5.3 SIGNAL PROCESSOR**

There were no test anomalies or failures during the scan drive part of the testing of the signal processor for this instrument.

## **5.4 TRANSISTOR ASSEMBLY**

There were no test anomalies or failures during testing of the transistor assembly for this instrument.

## **5.5 ANTENNA SUBSYSTEM**

There were no test anomalies or failures during testing of the antenna drive subsystem for this instrument. A discussion of test results is given in paragraphs 5.5.1 through 5.5.5.

### **5.5.1 SCAN MOTION AND JITTER**

In this test, the antenna position is measured in a series of five full scans. The measurement was made with the continuous rotation test pot temporarily affixed to the motor shaft. A Dynamic Signal Analyzer (DSA) is connected to the pot wiper to record the antenna position. Five scans were captured and stored on the AMSU-A2 Test Data File disc. One representative pattern is presented in Appendix B1.

Each 3.33 degree scene step was expanded in order to verify risetime, overshoot and jitter. The risetime limit is 42 ms, the jitter limits are  $\pm 5\%$  and the overshoot limit is 4 % above the upper jitter limit. The expanded waveforms were plotted and are presented in Appendices B2 through B59. All of the scene steps meet the step response requirements.

Slew periods to the cold and warm calibration stations were measured and met requirements. A time of 0.21 s is allowed for the 35 degree slew to cold cal, and 0.4 s for the 96.67 degree slew to warm cal. Calibration station jitter is less than the  $\pm 5\%$  maximum allowed. Expanded waveforms were plotted and are presented in Appendices B60 and B61. The waveforms are also stored on the AMSU-A2 Test Data File disc. The test data sheet is presented in Appendix B62.

### 5.5.2 PULSE LOAD BUS PEAK CURRENT AND RISE TIME

The peak current must be less than 2 A at any beam position along the scan, and it was measured to be 1.988 A. The current risetime while transitioning from one beam position to the next, and the risetime at the start and stop of the slew to warm cal position must be greater than 70  $\mu$ s. One 3.33° step was selected, and the risetime is 2.34 ms. For the slew to warm cal, the times are 2.43 ms and 2.34 ms for start and stop respectively.

The full scan pulse load bus current waveform is presented in Appendix C1, and the TDS is presented in Appendix C2. The waveform is also stored on the AMSU-A2 Test Data File disc.

### 5.5.3 RESOLVER READING AND POSITION ERROR

Reflector positions are obtained by using the STE, which displays the resolver readings to be compared with the position commands. Two readings are taken, one at the start of integration (LOOK 1), and the other halfway into the integration period (LOOK 2). The limits on the difference between the reported position (actual) and the command are  $\pm 10$  counts for LOOK 1 and  $\pm 5$  counts for LOOK 2. A table of reflector position commands and the reported position obtained from the STE computer printout is shown in Table 2, together with the differences between actual and command.

		Actual		Difference*				Actual		Difference*	
BP	Command	Look 1	Look 2	Look 1	Look 2	BP	Command	Look 1	Look 2	Look 1	Look 2
1	8368	8369	8369	1	1	19	5638	5642	5639	4	1
2	8216	8218	8216	2	0	20	5486	5488	5486	2	0
3	8064	8068	8064	4	0	21	5334	5338	5334	4	0
4	7913	7918	7914	5	1	22	5183	5187	5183	4	0
5	7761	7762	7761	1	0	23	5031	5034	5031	3	0
6	7609	7613	7610	4	1	24	4879	4882	4879	3	0
7	7458	7462	7459	4	1	25	4728	4732	4728	4	0
8	7306	7309	7306	3	0	26	4576	4579	4576	3	0
9	7154	7157	7155	3	1	27	4424	4428	4425	4	1
10	7003	7008	7004	5	1	28	4273	4277	4274	4	1
11	6851	6855	6852	4	1	29	4121	4123	4121	2	0
12	6699	6703	6699	4	0	30	3969	3972	3970	3	1
13	6548	6552	6548	4	0	WC	14361	14362	14362	1	1
14	6396	6396	6396	0	0	CC1	2376	2377	2377	1	1
15	6244	6247	6244	3	0	CC2	2452	2451	2451	-1	-1
16	6093	6098	6093	5	0	CC3	2528	2529	2529	1	1
17	5941	5945	5941	4	0	CC4	2679	2680	2680	1	1
18	5789	5792	5789	3	0						

BP = Beam position

\*Actual - Command

Table 2. Reflector (Beam) Position Commands and Measurements

#### 5.5.4 GAIN AND PHASE MARGIN

The gain and phase margin test is performed on the position control loop of the antenna drive subsystem. Three separate open loop gain and phase plots (measured with the loop closed) are obtained. The DSA is used to make the plots using the swept sine mode. Gain margin is measured at the  $-180^\circ$  phase crossover frequency, and phase margin is measured at the 0 dB gain crossover frequency. The margins on each of the three plots are above the minimum specification requirement of 12 dB and 25 degrees for the gain and phase respectively. The plots are presented in Appendices D1 through D6, and the TDS is presented in Appendix D7. The plots are also stored on the AMSU-A2 Test Data File disc.

#### 5.5.5 OPERATIONAL GAIN MARGIN

The operational gain margin test is also done three times. This test consists of increasing the gain inside the rate loop until oscillation occurs. The gain increase is calculated and the frequency of oscillation is measured from the spectrum plot using the DSA. An increase in gain greater than 9 dB is required, and the frequency of oscillation is just recorded.

To increase the gain, a 50 k $\Omega$  pot is connected in series with the R58 feedback resistor on amplifier AR8 on the R/D Converter/Oscillator CCA. The resistance of the test pot is slowly added to the feedback resistor while observing the reflector for oscillations. The reflector begins to produce an audible sound as gain is increased to the point of oscillation. Table 3 shows the added resistance values and the calculated gain margin.

Resistance (k $\Omega$ )	Gain Margin (dB)
40.315	9.51
40.877	9.59
39.805	9.44

Table 3. Pot Resistance and Operational Gain Margin

The first mode mechanical resonance of the shaft and reflector is about 78 Hz as shown in the power spectrum. The spectrum was plotted and is presented in Appendix E1, and the TDS is presented in Appendix E2. The spectrum plot is also stored on the AMSU-A2 Test Data File disc.

## **6.0 CONCLUSION**

Based on the test results, it can be concluded that the METSAT AMSU-A2 S/N 108 antenna drive subsystem meets the AMSU-A specification requirements.

## **7.0 TEST DATA**

Test data for the CCAs and the antenna drive subsystem is presented in the appendices as outlined in the Appendix Index on the following page.

**APPENDIX INDEX**

<i>Appendix A1 .....</i>	<i>Resolver Data Isolator CCA TDS</i>
<i>Appendix A2 .....</i>	<i>Interface Converter CCA TDS</i>
<i>Appendix A3 .....</i>	<i>Motor Driver 3-Hall Sensor CCA TDS</i>
<i>Appendix A4 .....</i>	<i>R/D Converter/ Oscillator CCA TDS</i>
<i>Appendix B1 .....</i>	<i>Full Scan Step Response</i>
<i>Appendix B2 thru B59 .....</i>	<i>Single Step Responses</i>
<i>Appendix B60 .....</i>	<i>Cold Calibration Step Response</i>
<i>Appendix B61 .....</i>	<i>Warm Calibration Step Response</i>
<i>Appendix B62 .....</i>	<i>Scan Motion and Jitter TDS</i>
<i>Appendix C1 .....</i>	<i>Peak Pulse Load Bus Current Waveform</i>
<i>Appendix C2 .....</i>	<i>Pulse Load Bus Current TDS</i>
<i>Appendix D1 thru D6 .....</i>	<i>Gain and Phase Margin Plots</i>
<i>Appendix D7 .....</i>	<i>Gain and Phase Margin TDS</i>
<i>Appendix E1 .....</i>	<i>Operational Gain Margin Power Spectrum</i>
<i>Appendix E2 .....</i>	<i>Operational Gain Margin TDS</i>



TEST DATA SHEET B-6 (Sheet 1 of 2)

RESOLVER DATA ISOLATOR CCA (P/N 1334972) (Paragraph 6.6.7)

Date: 7/28/97  
S/N: F33  
1334972-1  
6.6.7.1 Supply Voltages

Supply*	Measured Value (V)	Limits (Vdc)	Pass/Fail
+5 V (I)	+5.00	$\pm 0.25$	P
+5 V (U)	+5.06	$\pm 0.25$	P

6.6.7.2 Supply Currents

Steps 1 and 2:

Supply*	Measured Value (mA)	Limits (mA)	Pass/Fail
+5 V (I)	53.47	100 max	P
+5 V (U)	324.10	400 max	P

Steps 3 and 4:

Supply*	Measured Value (mA)	Limits (mA)	Pass/Fail
+5 V (I)	82.91	150 max	P
+5 V (U)	11.94	30 max	P

\* I = Isolated, U = Unisolated

6.6.7.3 Resolver Data

Bit No.	Pass/Fail
API 0 - AP Bit 0	P
API 1 - AP Bit 1	P
API 2 - AP Bit 2	P
API 3 - AP Bit 3	P
API 4 - AP Bit 4	P
API 5 - AP Bit 5	P
API 6 - AP Bit 6	P
API 7 - AP Bit 7	P
API 8 - AP Bit 8	P
API 9 - AP Bit 9	P
API 10 - AP Bit 10	P
API 11 - AP Bit 11	P
API 12 - AP Bit 12	P
API 13 - AP Bit 13	P

6.6.7.4 Converter Busy Pulse

Converter Busy Pulse	Measured Value ( $\mu$ sec)	Limits ( $\mu$ sec)	Pass/Fail
15.0	14.45	$\pm 3.0$	P

123 23345



TEST DATA SHEET B-6 (Sheet 2 of 2)

RESOLVER DATA ISOLATOR CCA (P/N 1334972) (Paragraph 6.6.7)

Comments:

NONE

Conducted by:

Test Engineer

Date

Verified by:

Quality Control Inspector

Date

Approved by:

DCMC

Date

TEST DATA SHEET B-13 (Sheet 1 of 3)

INTERFACE/CONVERTER CCA (P/N 1331697) (Paragraph 6.13.7)

Date: 8/6/97  
CCA S/N: F25  
1331697-1

6.13.7.1 Supply Voltages

Supply	Measured Value (Vdc)	Limits (Vdc)	Pass/Fail
+5V (U)	5.00	+5V±0.05	P
+15V (I)	15.00	+15V±0.15	P
-15V (I)	-14.97	-15V±0.15	P
+5V (I)	5.02	+5V±0.05	P

6.13.7.2 Supply Currents

Step 1 (CP and API Low):

Supply	Measured Value (mA)	Limits (mA)	Pass/Fail
+5V (U)	86.70	70 - 110	P
+5V (I)	3.40	1.5 - 5.5	P
+15V (I)	17.96	15 - 23	P
-15V (I)	20.57	18 - 26	P

Step 2 (CP and API High):

Supply	Measured Value (mA)	Limits (mA)	Pass/Fail
+5V (U)	56.65	40 - 70	P
+5V (I)	23.96	18 - 30	P
+15V (I)	17.96	15 - 23	P
-15V (I)	20.57	18 - 26	P

6.13.7.3 Amplifier Offsets

Amplifier	Measured Value (mV)	Limits (mV)	Pass/Fail
AR1	+0.14	0.0±0.15	P
AR2	-0.10	0.0±2.0	P

## TEST DATA SHEET B-13 (Sheet 2 of 3)

## INTERFACE/CONVERTER CCA (P/N 1331697) (Paragraph 6.13.7)

## 6.13.7.4 Subtraction and D-A Conversion

Step 1:

unturned  
9-10-97  
 $\pm 0.00015$   
 $\pm 0.00060$   
 $\pm 0.00030$

Actual Position (API) MSB      LSB	Command Position (CP) MSB      LSB	ARI Output Voltage Required (Vdc)	Test Result (Vdc)	Pass/Fail
00000000000000	00000000000000	0.00000	+0.00014	P
00000000000001	00000000000000	-0.00061	-0.000433	P
00000000000010	00000000000000	-0.00122	-0.001056	P
00000000000011	00000000000000	-0.00184	-0.001688	P
00000000000100	00000000000000	-0.00245	-0.002310	P
00000000001000	00000000000000	-0.00490 *	-0.004797	P
00000000010000	00000000000000	-0.00979 *	-0.009764	P
00000000100000	00000000000000	-0.01958 *	-0.019700	P
00000001000000	00000000000000	-0.03917 *	-0.039572	P
00000010000000	00000000000000	-0.07834 *	-0.079323	P
00000100000000	00000000000000	-0.15667 *	-0.15882	P
00001000000000	00000000000000	-0.31334 *	-0.31785	P
00010000000000	00000000000000	-0.62669 *	-0.63599	P
00100000000000	00000000000000	-1.25338 *	-1.2723	P
01000000000000	00000000000000	-2.50675 *	-2.5447	P
10000000000000	00000000000000	-5.01350 *	-5.0899	P

\* Tolerance on output voltage is  $\pm 10\%$ 

Step 2:

unturned  
9-10-97  
 $\pm 0.00015$   
 $\pm 0.00060$   
 $\pm 0.00030$

Actual Position (API) MSB      LSB	Command Position (CP) MSB      LSB	ARI Output Voltage Required (Vdc)	Test Result (Vdc)	Pass/Fail
00000000000000	00000000000000	0.00000	+0.00014	P
00000000000000	00000000000001	0.00061	+0.000756	P
00000000000000	00000000000010	0.00122	+0.001390	P
00000000000000	00000000000011	0.00184	+0.002003	P
00000000000000	00000000000100	0.00245	+0.002628	P
00000000000000	00000000001000	0.00490 *	+0.005113	P
00000000000000	00000000010000	0.00979 *	+0.010100	P
00000000000000	00000000100000	0.01958 *	+0.020042	P
00000000000000	00000001000000	0.03917 *	+0.039926	P
00000000000000	00000010000000	0.07834 *	+0.079668	P
00000000000000	00000100000000	0.15667 *	+0.15924	P
00000000000000	00001000000000	0.31334 *	+0.31833	P
00000000000000	00010000000000	0.62669 *	+0.63653	P
00000000000000	00100000000000	1.25338 *	+1.2727	P
00000000000000	01000000000000	2.50675 *	+2.5452	P
00000000000000	10000000000000	-5.01350 *	-5.0899	P

\* Tolerance on output voltage is  $\pm 10\%$

19 Jun 97

## TEST DATA SHEET B-13 (Sheet 3 of 3)

## INTERFACE/CONVERTER CCA (P/N 1331697) (Paragraph 6.13.7)

6.13.7.5 Strobe FunctionStep 1: Strobe LowNo E11 Change  
with Input CP ChangesPass/FailPStep 2: Strobe HighE11 Change  
with Input CP ChangesPass/FailP6.13.7.6 Amplifier Gain

	<u>Measured Value (Vdc)</u>	<u>Limits (Vdc)</u>	<u>Pass/Fail</u>
E11	<u>0.31833</u>	-	<u>P</u>
E10	<u>3.4931</u>	-	<u>P</u>
E10 Voltage E11 Voltage	<u>10.97</u>	10.7 - 11.3	<u>P</u>

6.13.7.7 Ground Isolation

	<u>Measured Value (MΩ)</u>	<u>Limits (MΩ)</u>	<u>Pass/Fail</u>
Pin 91 to Pin 7 DC Resistance	<u>larger than 115MΩ</u>	>20	<u>P</u>

Comments:

NONE

Conducted by:

Denise L...  
Test Engineer8/6/97  
Date

Verified by:

Richard Huth (7A 190)  
Quality Control Inspector10/10/97  
Date

Approved by:

Richard Thomas  
DCMC10/14/97  
Date

TEST DATA SHEET B-4 (Sheet 1 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

S/N: F02  
Date: 4/17/97  
1331694-4  
6.4.3.2 Input Signal Offset

Step No.	Test Results	Limits
4	1.21 mV	0.0 ± 1 mVdc
6	1.41 mV	0.0 ± 1 mVdc
8	0.93 mV	0.0 ± 1 mVdc

Step No.	Test Resistor	Resistance Measured
13	E7-ES (R25)	3.16K
	E9-E10 (R52)	4.80K
	E11-E12 (R33)	3.40K
	E13-E14 (R53)	5.80K
	E15-E16 (R42)	3.16K
	E17-E18 (R54)	4.30K

Step No.	Resistors	Selected Trim Resistors
14	R25	RNC55J3161FS
	R52	RNC55J4751FS
	R33	RNC55J3401FS
	R53	RNC55J5621FS
	R42	RNC55J3161FS
	R54	RNC55J4221FS

Step No.	E Point	Test Results	Limits	Pass/Fail
19	E19	-0.01 mV	0.0 ± 1 mVdc	P
	E20	0.02 mV	0.0 ± 1 mVdc	P
	E21	0.04 mV	0.0 ± 1 mVdc	P

6.4.3.3 Motor Driver Operation

Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
2	5.00 V	+5V ± 0.05Vdc	P
	52.6 mA	70mAdc max	P
	15.07V	+15V ± 0.15Vdc	P
	1.5 mA	3.0mAdc max	P
	-14.98V	-15V ± 0.15Vdc	P
	18.5 mA	25mAdc max	P
	28.03V	+28V ± 0.5Vdc	P
	5.6 mA	8mAdc max	P
3	275 mV	400mVdc max	P
4	42.2 mA	50mAdc max	P
5	47.2 mA	50mAdc max	P

TEST DATA SHEET B-4 (Sheet 2 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

Counter Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
3	280 mV	400mVdc max	P
4	35.6 mA	50mAdc max	P
5	40.0 mA	50mAdc max	P

6.4.3.4 Current Limit Test

Step No.	Test Results	Limits	Pass/Fail
2	460 mA	350-500mAdc	P

Comments:

NONE

Conducted by:

Test Engineer

Date

Verified by:

Quality Control Inspector

Date

Approved by:

DCMS

Date

TEST DATA SHEET B-4 (Sheet 1 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

S/N: F03  
Date: 4/17/97  
1331694-4  
6.4.3.2 Input Signal Offset

Step No.	Test Results	Limits
4	1.49 mV	0.0 ± 1 mVdc
6	1.00 mV	0.0 ± 1 mVdc
8	1.45 mV	0.0 ± 1 mVdc

Step No.	Test Resistor	Resistance Measured
13	E7-E8 (R25)	2.80k
	E9-E10 (R52)	4.50k
	E11-E12 (R33)	2.80k
	E13-E14 (R53)	3.80k
	E15-E16 (R42)	2.80k
	E17-E18 (R54)	4.35k

Step No.	Resistors	Selected Trim Resistors
14	R25	RNC55J2801FS
	R52	RNC55J4531FS
	R33	RNC55J2801FS
	R53	RNC55J3741FS
	R42	RNC55J2801FS
	R54	RNC55J4221FS

Step No.	E Point	Test Results	Limits	Pass/Fail
19	E19	- 0.05 mV	0.0 ± 1 mVdc	P
	E20	+ 0.05 mV	0.0 ± 1 mVdc	P
	E21	+ 0.06 mV	0.0 ± 1 mVdc	P

6.4.3.3 Motor Driver Operation

Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
2	+5.00V	+5V ± 0.05Vdc	P
	52.4 mA	70mA dc max	P
	+15.07V	+15V ± 0.15Vdc	P
	1.6 mA	3.0mA dc max	P
	-15.00V	-15V ± 0.15Vdc	P
	18 mA	25mA dc max	P
	+28.04V	+28V ± 0.5Vdc	P
	6 mA	8mA dc max	P
3	+ 277 mV	400mVdc max	P
4	42.1 mA	50mA dc max	P
5	47.5 mA	50mA dc max	P

AE-26693A  
10 Feb 97

TEST DATA SHEET B-4 (Sheet 2 of 2)

MOTOR DRIVER 3-HALL SENSOR CCA (P/N 1331694) (Paragraph 6.4.3)

Counter Clockwise Rotation:

Step No.	Test Results	Limits	Pass/Fail
3	300mV	400mVdc max	P
4	37.2mA	50mAdc max	P
5	38.8mA	50mAdc max	P

6.4.3.4 Current Limit Test

Step No.	Test Results	Limits	Pass/Fail
2	450mA	350-500mAdc	P

Comments:

NONE

Conducted by:

*Dennis*  
Test Engineer

(7A)  
(269)

4/17/97  
Date

Verified by:

*Judith Hervey*  
Quality Control Inspector

04/28/97  
Date

Approved by:

*[Signature]*  
DCMC

4/29/97  
Date

## TEST DATA SHEET B-5 (Sheet 1 of 3)

## R-D CONVERTER/OSCILLATOR CCA (P/N 1337739) (Paragraph 6.5.7)

Date 8/26/97CCA S/N F11  
1337739-26.5.7.1 UUT Pre-Test

## Step 2:

## Supply Currents (Without UUT)

Supply (Vdc)	(Baseline) Measured Value (mA) (Without UUT)	Limits (mA)	Pass/Fail
+15	0.06	0-1	P
-15	-0.28	-1 - 0	P
+5	0.06	0-1	P

## Supply Voltages (Without UUT)

Supply	Measured Value (V)	Limits (V)	Pass/Fail
+15V (I)	15.02	± 0.50	P
-15V (I)	-15.02	± 0.50	P
+5V (I)	5.03	±0.25	P

## Step 6:

## Supply Currents (UUT Installed)

Supply (Vdc)	Measured Value (mA) (UUT Installed)	Difference (mA) (Measured - Baseline)	Limits (mA)	Pass/Fail
+15	32.20	32.14	20-40	P
-15	-37.84	-37.56	-30 - -50	P
+5	56.76	56.70	30-70	P

6.5.7.2 Supply Voltages (UUT Installed)

Supply	Measured Value (V)	Limits (V)	Pass/Fail
+15V (I)	15.01	± 0.50	P
-15V (I)	-14.97	± 0.50	P
+5V (I)	5.02	±0.25	P

6.5.7.3 Oscillator Frequency, Duty Cycle, and Output Voltage

Parameter	Measured Value	Limits	Pass/Fail
Frequency	1610 Hz	1550-1650 Hz	P
Duty Cycle	51.7 %	45-55 %	P
Output Voltage	8.03 V	7.6-8.4 Vrms	P

TEST DATA SHEET B-5 (Sheet 2 of 3)

R-D CONVERTER/OSCILLATOR CCA (P/N 1337739) (Paragraph 6.5.7)

6.5.7.4 R-D Converter Operation

Step 1:

Bit Number/ Test Fixture Label	CW Pass/Fail	CCW Pass/Fail
API 0/1	P	P
API 1/2	P	P
API 2/3	P	P
API 3/4	P	P
API 4/5	P	P
API 5/6	P	P
API 6/7	P	P
API 7/8	P	P
API 8/9	P	P
API 9/10	P	P
API 10/11	P	P
API 11/12	P	P
API 12/13	P	P
API 13/14	P	P
Converter Busy	P	P

Step 2:

RS (E10)	Measured Value (Vdc)	Calculated Value (Vdc) * CCA -1 Assy	Calculated Value (Vdc) * CCA -2 Assy	Pass/Fail
CW Rotation**	1.557	(+) N/A	(+) 1.790	P
CCW Rotation**	-1.846	(-) N/A	(-) 1.790	P

\* Signal level function of test and calibration gain resistors. Record calculated value and measured value. Measured value shall be within  $\pm 10\%$  percent of calculated value. The equation is as follows:

$$V = \pm 0.155 \left( \frac{R20}{R17} \right) \pm 10\%$$

$$= 0.155 \left( \frac{59k}{5.11k} \right) = 1.790V$$

30  
 (223) *unlabeled*  
 8-26-97

30  
 (223) *unlabeled*  
 8-26-97

6.5.7.5 Amplifier Gain

PES-RS	Measured Value (Vdc)	Limits (Vdc)	Pass/Fail
PES = +0.300 Vdc	1.168	1.00 to 1.30	P
PES = -0.300 Vdc	1.064	1.00 to 1.30	P

6.5.7.6 Direction Control Signal

DIR CNTRL	Measured Value (Vdc)	Limits (Vdc)	Pass/Fail
CW Rotation	5.000V	4.5 to 5.5	P
CCW Rotation	0.132V	0.0 to 0.4	P

TEST DATA SHEET B-5 (Sheet 3 of 3)

R-D CONVERTER/OSCILLATOR CCA (P/N 1337739) (Paragraph 6.5.7)

6.5.7.7 Notch Filter Frequency Response

Frequency	Measured Value (Hz)	Calculated Value (Hz) * CCA -1 Assy	Calculated Value (Hz) * CCA -2 Assy	Pass/Fail
AR3 Notch	N/A	N/A	N/A	N/A
AR4 Notch	↓	↓	↓	↓
AR5 Notch	↓	↓	↓	↓

\* Notch frequencies shall be within  $\pm 3$  percent of values determined by test and calibration resistors. Record calculated and measured values.

Comments:

NONE

Conducted by:

Test Engineer

*[Signature]*

Date

8/26/97

Verified by:

Quality Control Inspector

NOV 18 97

Date

Approved by:

DCMC

Date

11-14-97



X=7.1609 Sec  
Y=30.7563 V

r=43.6364m ΔY=21.82mV

CAP TIM BUF  
36.0

### Jitter and Overshoot Limits

Position 30 Volts - Position 1 Volts = 10.45 Volts

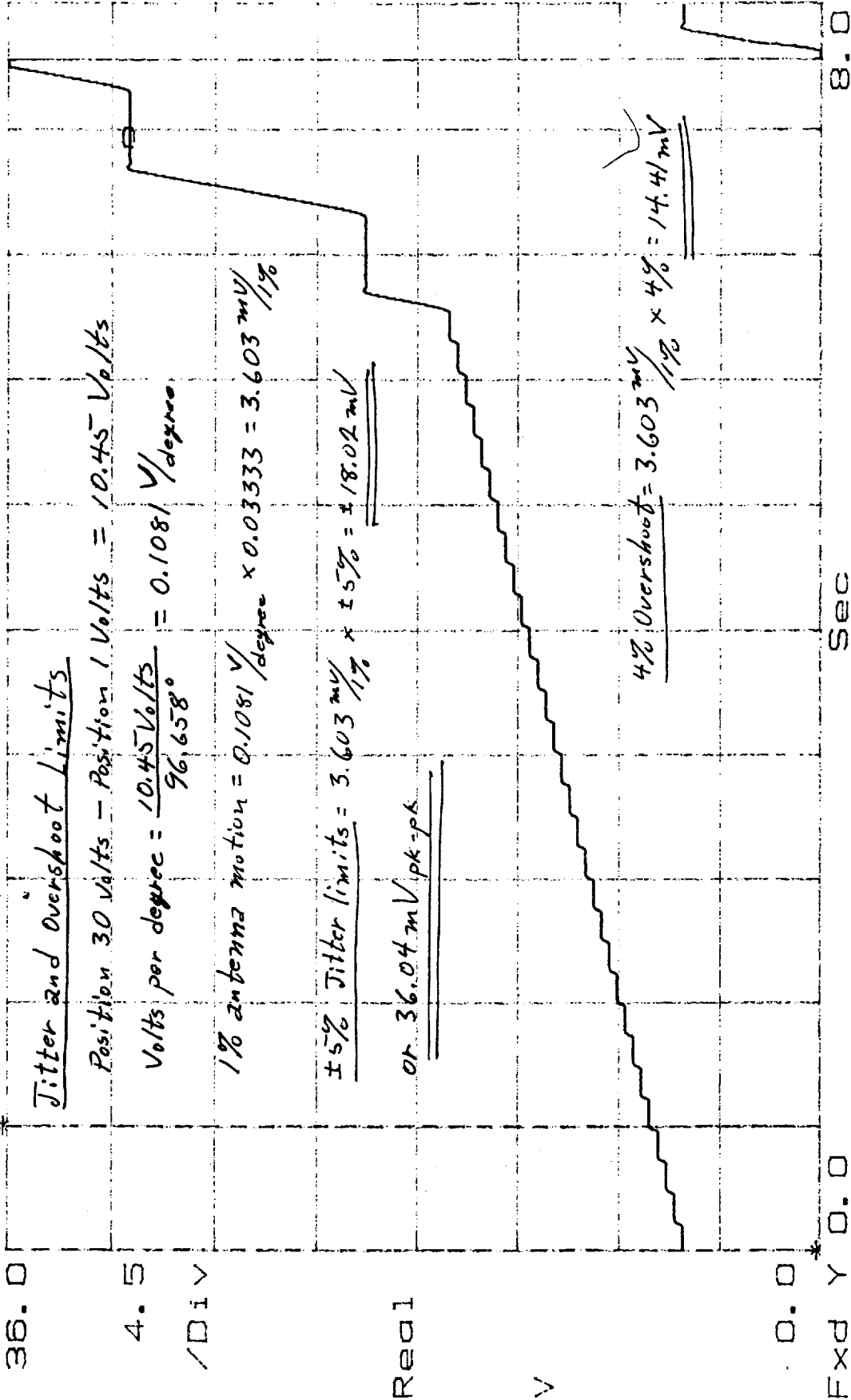
Volts per degree =  $\frac{10.45 \text{ Volts}}{96.658^\circ} = 0.1081 \text{ V/degree}$

1% antenna motion =  $0.1081 \text{ V/degree} \times 0.03333 = 3.603 \text{ mV/1\%}$

$\pm 5\%$  Jitter limits =  $3.603 \text{ mV/1\%} \times \pm 5\% = \pm 18.02 \text{ mV}$

or 36.04 mV pk-pk

4% Overshoot =  $3.603 \text{ mV/1\%} \times 4\% = 14.41 \text{ mV}$



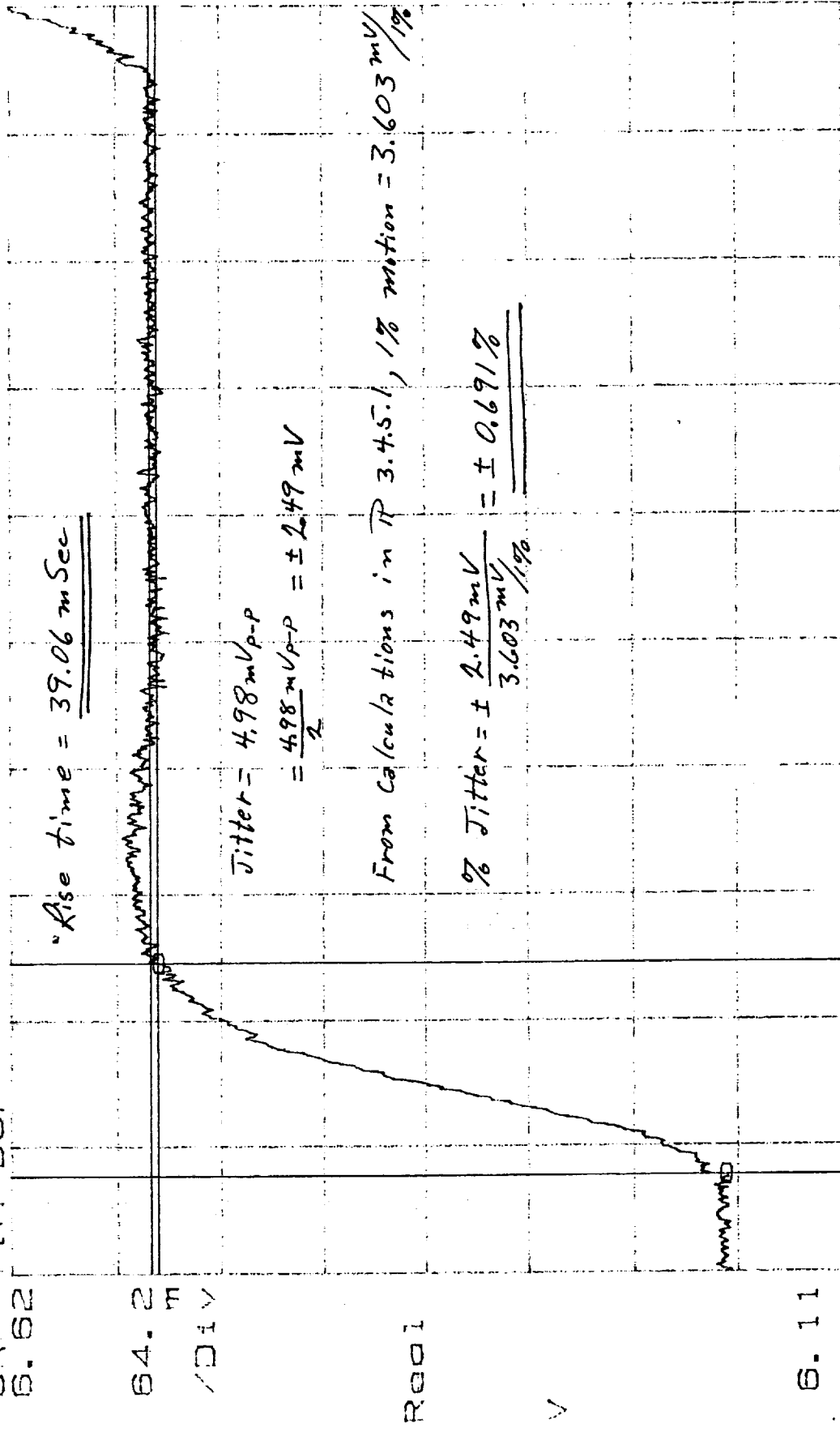
File: 6AP-FSP  
R 3.4.5.1  
SN: 108

Preliminary Antenna Position Full Scan

Test Eng: DeLund Date: 4/13/69  
Quality: (3.4)

X=163.7ms ΔX=39.06ms Y=6.54016 ΔY=4.98mV  
 Y0=6.18239 ΔY0=351.9mV

CAP TIM BUF  
 6.62



6.11

FxdXY 146m

Sec

7AP.FS5

377m

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

P 3.4.5.5

Step 1-2

Test Engr: D. S. S. S. Date: 4/29/84

Quality: 892

BZ

Y=6.5386  $\Delta Y=18.05mV$

CAP TIM BUF  
5.62

64.2  
m  
/DIV

Real

6.11

FxdXY 146m

Sec

7APFS5

377m

+5% limit

Overshoot = 0.0mV

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

$\overline{H}$  3.4.5.5

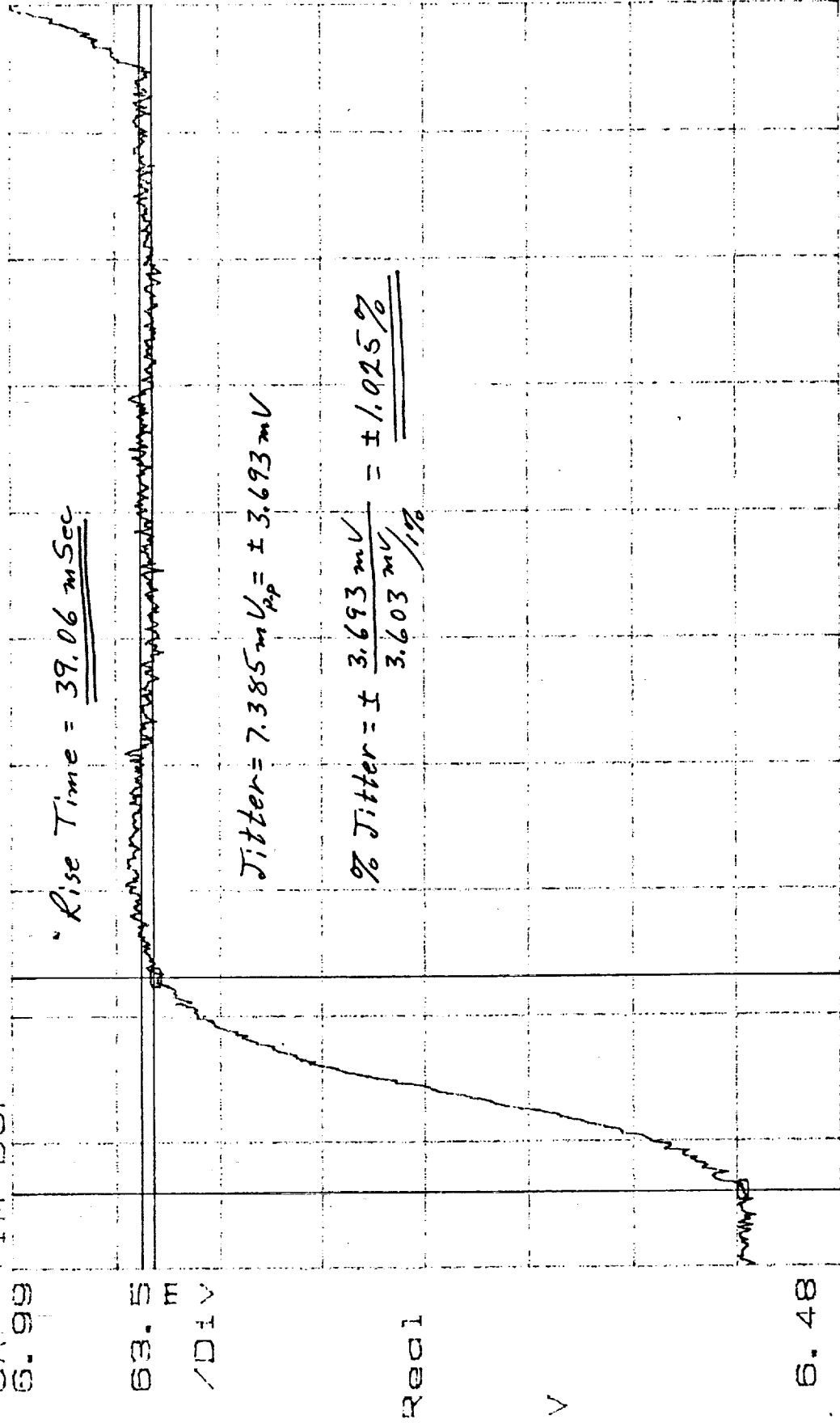
Step 1-2

Test Engr: D. Lind Date: 4/10/68

Quality: (2A) 268

X=403.9ms ΔX=39.06ms Y=6.90096 ΔY=7.385mV  
Y=6.89924 ΔY=360.0mV

CAP TIM BOF  
6.99



ExdY 351m 7AP\_F55

S/O: 335168  
P/N: 1331200-2-IT  
S/N: 108

Scan Motion and Jitter  
TA 3.4.5.5  
Step 2-3

Test Engr: D. L. L. Date: 4/20/89  
Quality: (TA 258)

B4

Y=6. 90311  $\Delta Y=18.16mV$

CAP TIM BUF  
6.99

63.5  
/DIV

Reul

6.48

EXDXY 351m

Sec

7APFS5

579m

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

TP 3.4.5.6

Step 2-3

Test Engr: Resend

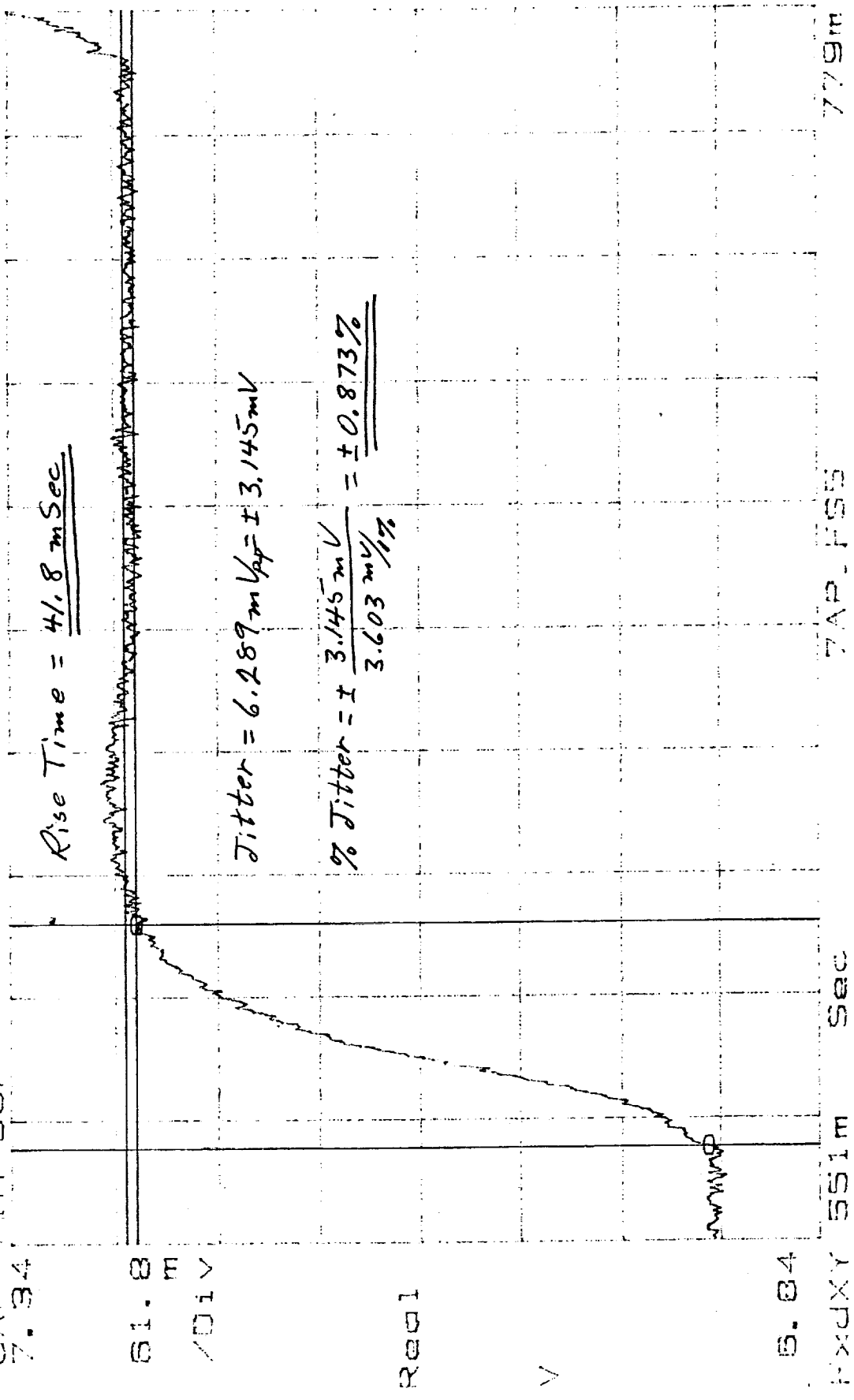
Quality: 268

Date: 4/20/97

B5

X=568.7ms    ΔX=41.8ms    Y=7.26147    ΔY=6.289mV  
 Y=6.91059    ΔY=350.3mV

CAP TIM BUF  
 7.34



51.8 mV  
 5.84  
 PXXXY 551m  
 7AP.F55  
 779m  
 Scan Motion and Jitter Test Engr: Daniel Date: 4/20/89  
 P/N: 1331200-2-IT  
 S/N: 108  
 H 3.4.5.5  
 Step 3-4  
 Quality: 89%

Y=7.26417  $\Delta Y=17.97mV$

CAP TIM BUF  
7.34

+5% limit

51.8  
m  
/DIV

Overshoot = 0.0 mV

Real

V

6.84

FXDXY 551m

Sec

7AP.F55

779m

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter  
P 3.4.5.5  
Step 3-4

Engr: D. Lenz

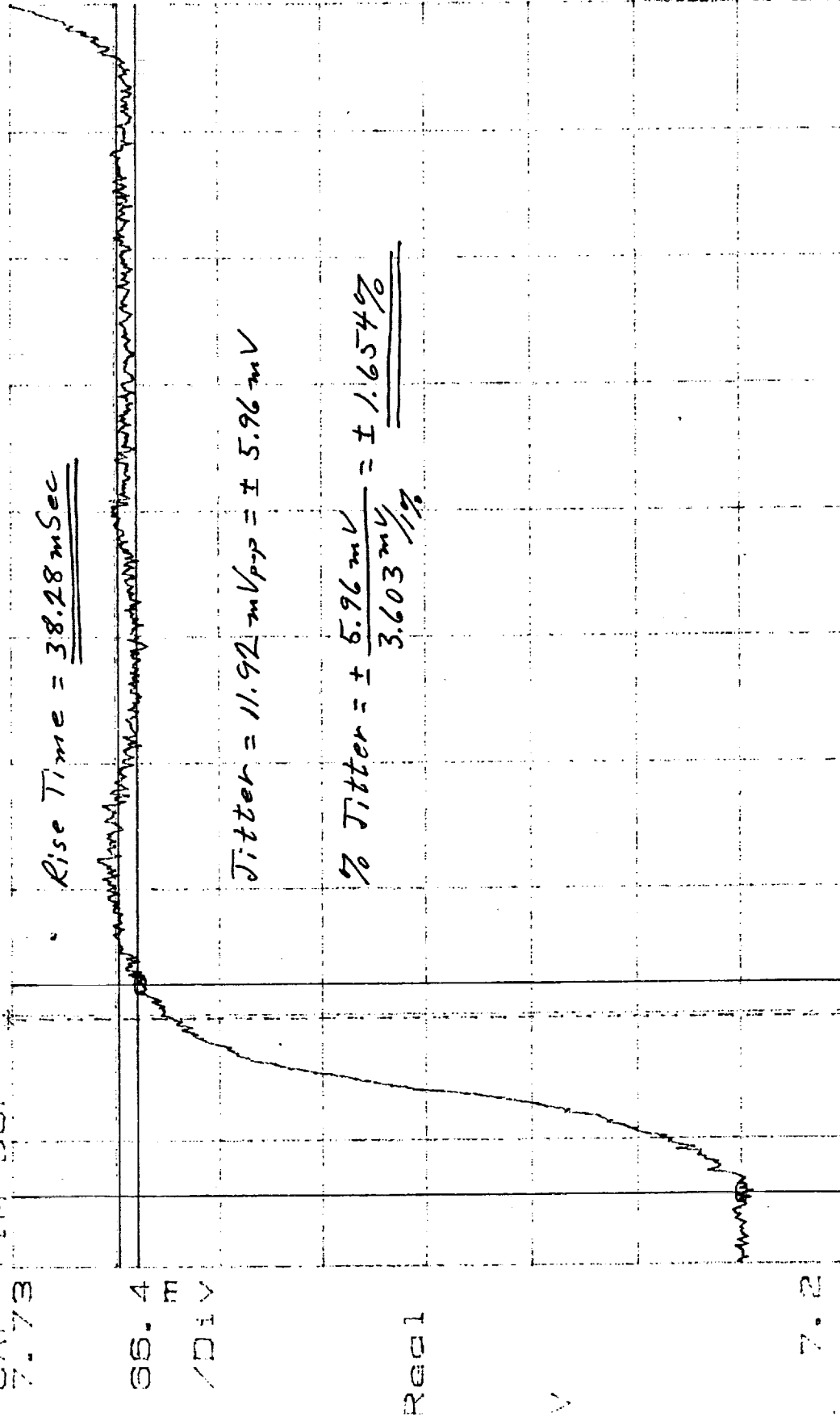
Quality: (79) 298

Date: 4/20/97

B7

X=806.2ms ΔX=38.28ms Y=7.65056 ΔY=11.92mV  
 Y0=7.64852 ΔY0=382.8mV

CAP TIM BUF  
 7.73



7.2

FWDXY 755m

Sec

7AP\_FSS

983m

S/O: 335/68

P/N: 133/100-2-IT

S/N: 108

Scan Motion and Jitter

HP 3.4.5.5

Step 4-5

Test Engr: D. Ford Date: 4/20/89

Quality: (892) VL

B8

Y=7.657  $\Delta Y=18.03mV$

CAP TIM BUF  
7.73

+5% limit

66.4mV  
/DIV

Overshoot = 0.0mV

Real

V

7.2

EXDXY 755m

Sec

7AP\_FS5

983m

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

HP 3.4.5.5

Step 4-5

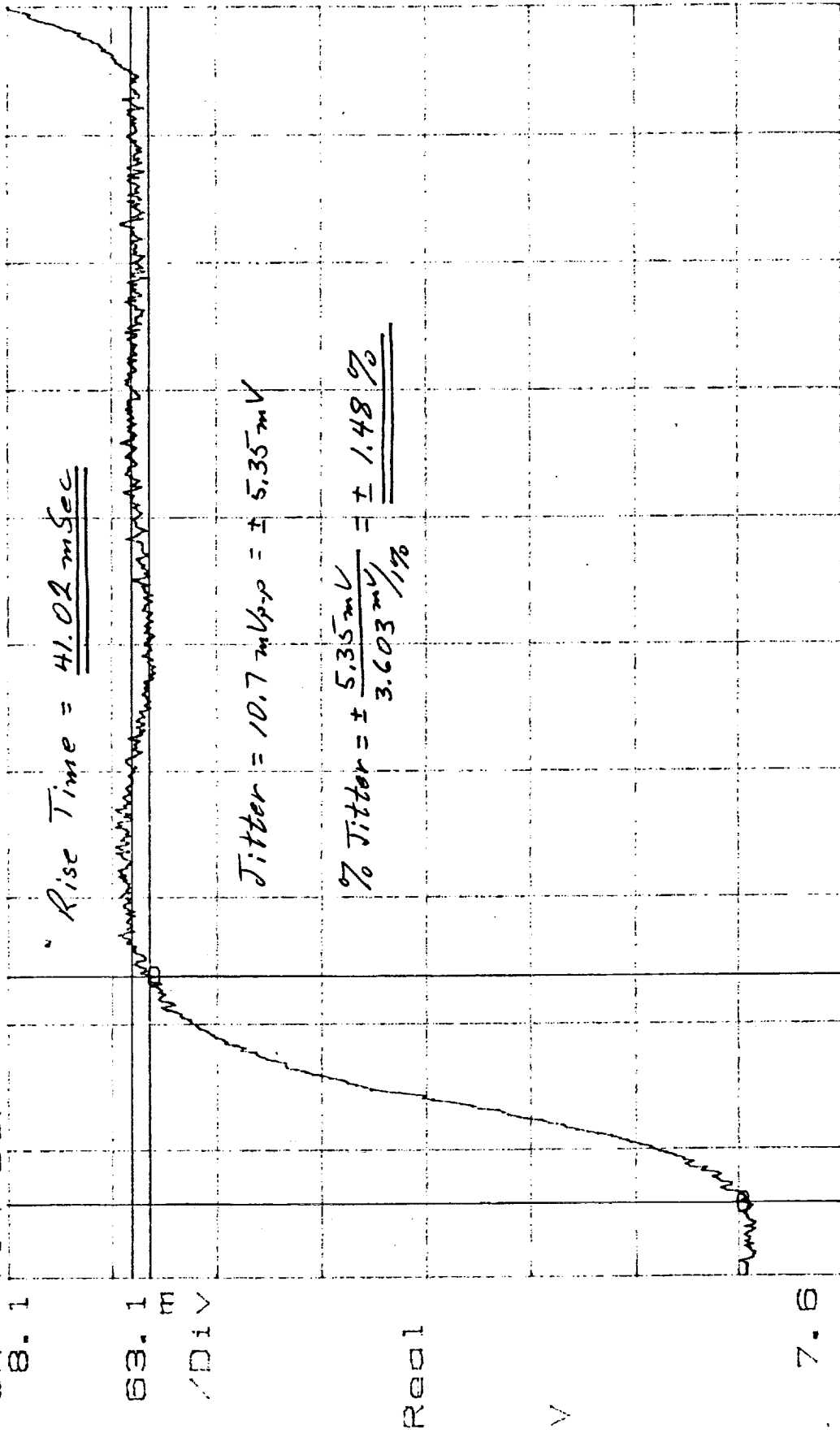
Test Engr: D. Lind

Quality: 1A  
266

Date: 4/2/87

X=1.013 S ΔX=41.02ms Y=8.01808 ΔY=10.7mV  
 Y=8.01505 ΔY=355.2mV

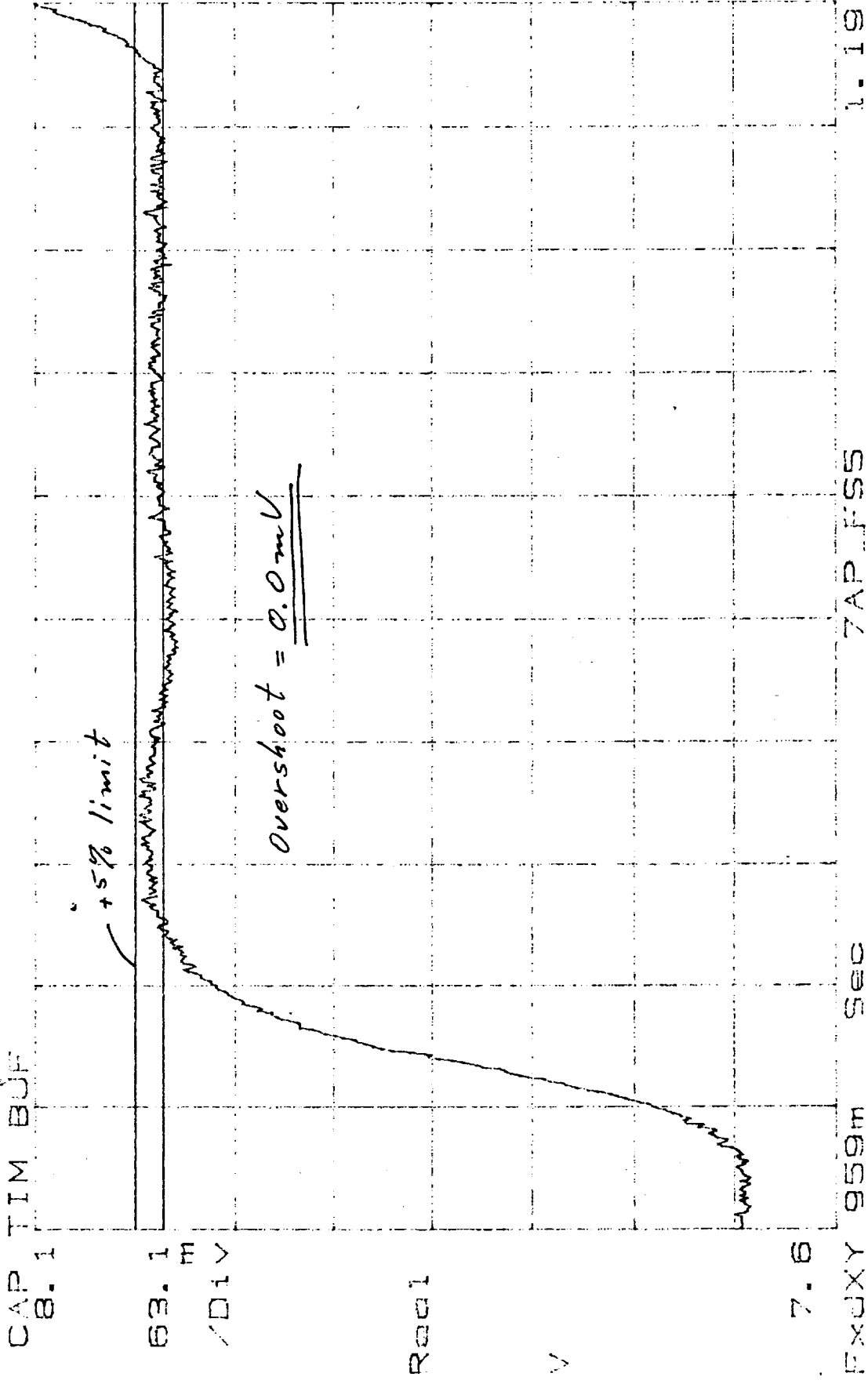
CAP TIM BUF  
 8.1



Real V 7.6 FxdY 959m 1.19

%: 335/68 Scan Motion and Jitter Test Engr: Desand Date: 4/8/88  
 P/N: 1331200-2-IT IP 3.4.5.5 Quality: 999  
 S/N: 108 Step 5-6 B10

Y=8.02328 ΔY=18.04mV



S/O: 335168  
P/N: 1331200-2-IT  
S/N: 108

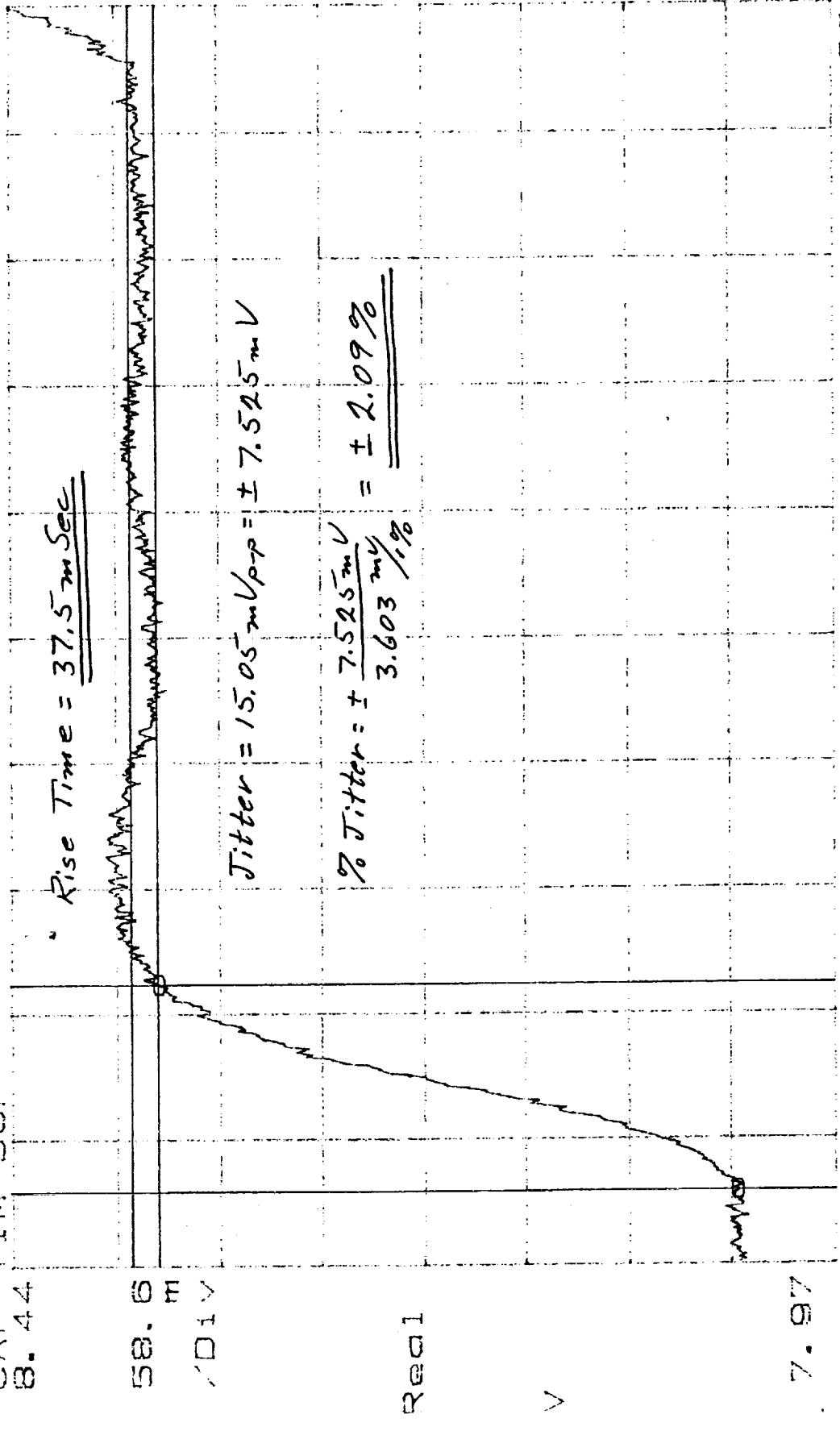
Scan Motion and Jitter  
IT 3.4.5.5  
Step 5-6

Test Engr: D. Ford Date: 4/20/98  
Quality: 260

B11

X=1.212 S    ΔX=37.5mS    Y=8.35828    ΔY=15.05mV  
 Y0=8.35726    ΔY0=330.9mV

CAP TIM BUF  
 8.44



ExdXY 1.16    7AP JMS5    1.39

S/O: 335768    Scan Motion and Jitter    Test Engr: D. Lenz    Date: 4/20/97  
 P/N: 1331200-2-IT    IT 3.4.5.5    Quality: 268  
 S/N: 108    step 6-7

Y=8.3668  $\Delta Y=18.18mV$

CAP TIM BUF  
8.44

58.6  
m  
/DIV

Real

V

7.97

EXDXY 1.16

Soc

7AP JFS5

1.39

+5% limit

Overshoot = 0.0mV

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

FP 3.4.5.5

Step 6-7

Test Engr: D. Ford

Date: 4/20/99

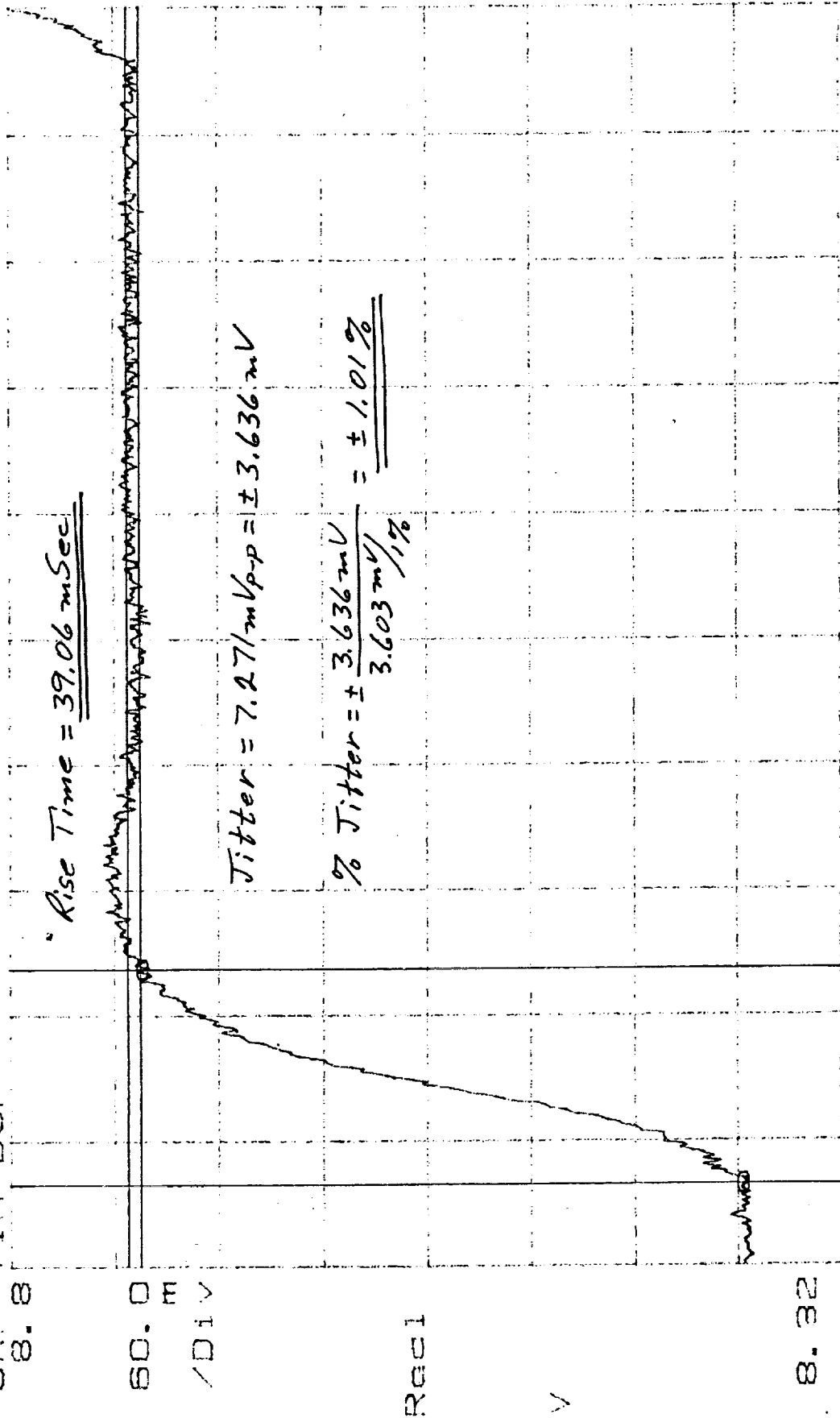
Quality: 3A

892

B13

X=1.377 S ΔX=39.06ms Y=8.72013 ΔY=7.271mV  
 Y<sub>a</sub>=8.37185 ΔY<sub>a</sub>=347.1mV

CAP TIM BUF  
 8.8



8.32

FXDXY 1.36

Sec

7AP\_F55

1.59

S/O: 335/68  
 P/N: 1331200-2-IT  
 S/N: 108

Scan Motion and Jitter  
 T 3.4.5.5  
 step 7-8

Test Engr: Q. Lind Date: 4/20/97  
 Quality: 7A ESR

B14

Y=8.7242  $\Delta Y=18.03mV$

CAP TIM BUF

8.8

50.0

m

/DIV

Real

V

8.32

EXDXY

1.36

Sec

7AP\_F55

1.59

+5% limit

Overshoot = 0.0mV

S/O: 335168

PN: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

RP 3.4.5.5

Step 7-8

Test Eng: <sup>(14)</sup>LO

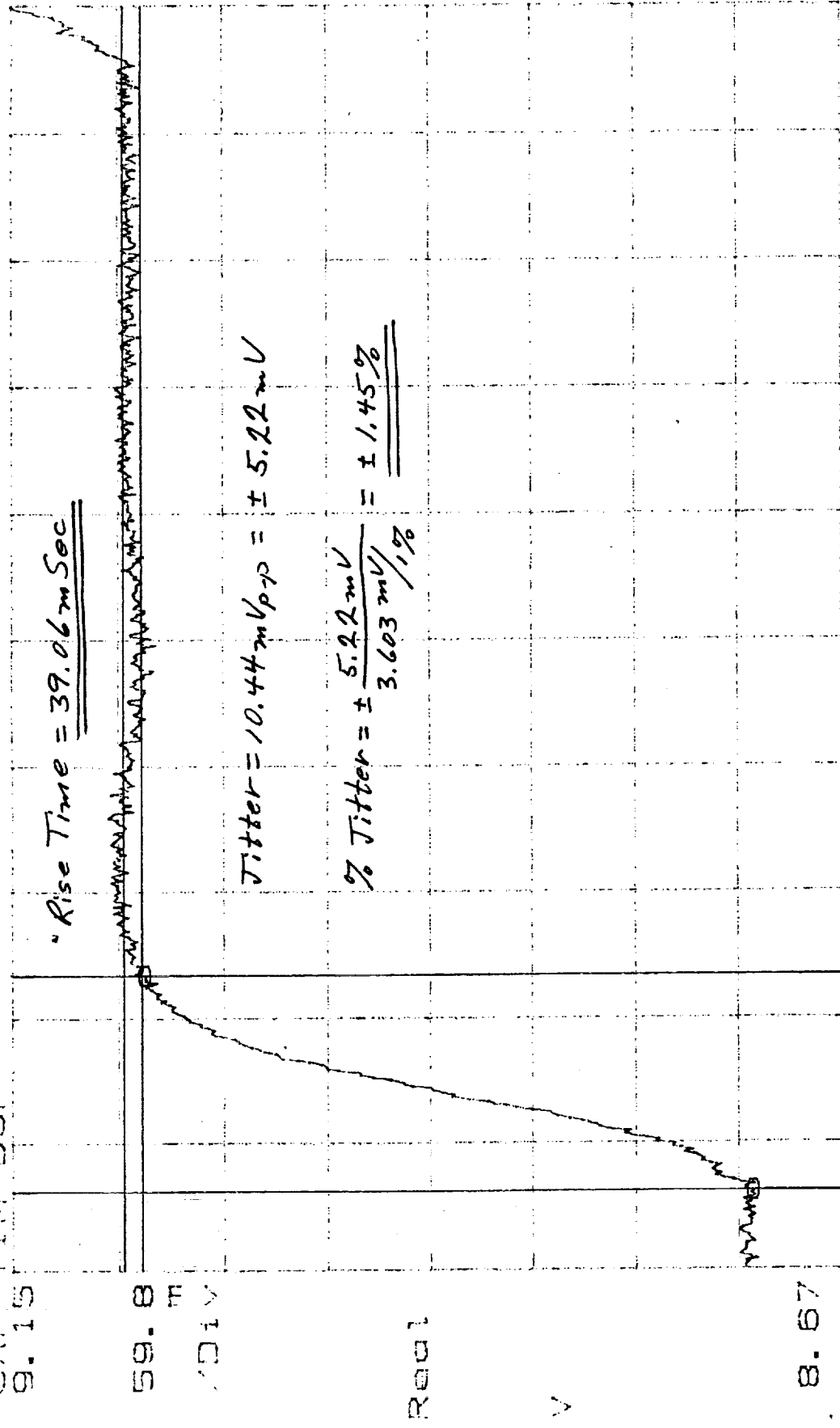
Quality: <sup>(28)</sup>

Date: 4/20/68

B/5

X=1.58 S ΔX=39.06ms Y=9.07566 ΔY=10.44mV  
 Y=8.72217 ΔY=351.9mV

CAP. TIM BUF  
 9.15



8.67

ENDY 1.57

Sec

7AP.F55

1.79

S/O: 335168

PN: 1331200-2-IT

SN: 108

Scan Motion and Jitter

R 3.455

Step 8-9

Test Engr: D. L. L. Date: 4/2/68

Quality: 7A  
268

B16

Y=9.0803  $\Delta Y=17.97\text{mV}$

CAP TIM BUF  
9.15

59.8 F  
/01V

Real

8.67

FMXKY 1.57

SEC

7AP\_FSS

1.79

+5% limit

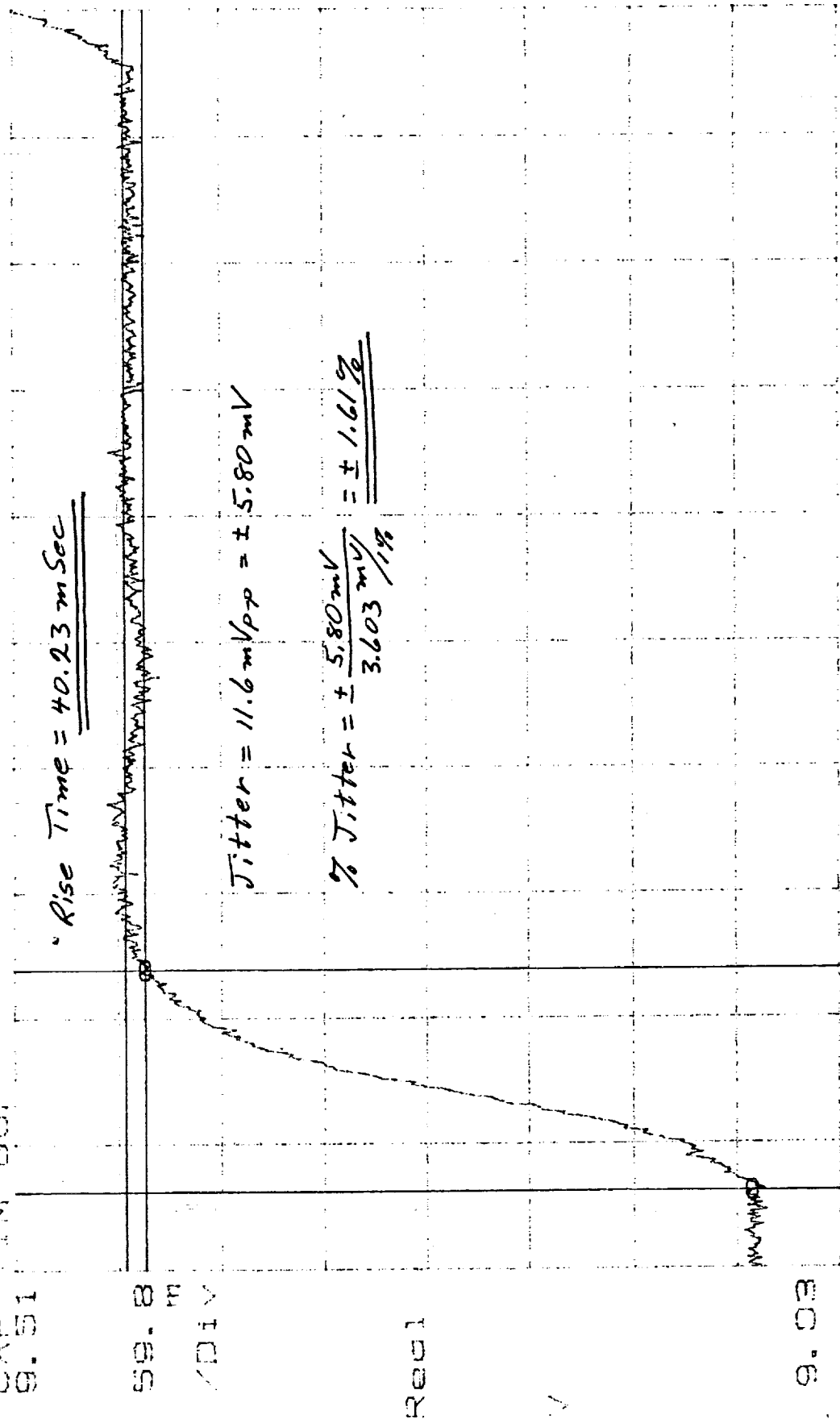
Overshoot = 0.0mV

S/O: 335168  
P/N: 1331200-2-IT  
S/N: 108  
Scan Motion and Jitter  
T 3.4.5.5  
Step 8-9  
Test Engr: D. Ford Date: 4/20/89  
Quality: 7A  
258

B17

X=1.823 S ΔX=40.23mS Y=9.4375 ΔY=11.6mV  
 Y=9.43739 ΔY=351.9mV

CAP. TIM BUI  
 9.51



9.03

EXDXY 1.77

Sec

7AP 1.55

2.0

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

PI 3.4.5.5

Step 9-10

Test Engr: D. Lind Date: 4/29/82

Quality: 7A 258

B18

Y=9.44388  $\Delta Y=17.98mV$

CAP TIM BUF  
9.51

59.8 m  
/DIV

Real

V

9.03

PRDXY 1.77

Sec

7AP. J.55

2.0

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

R 3.4.5.5

Step 9-10

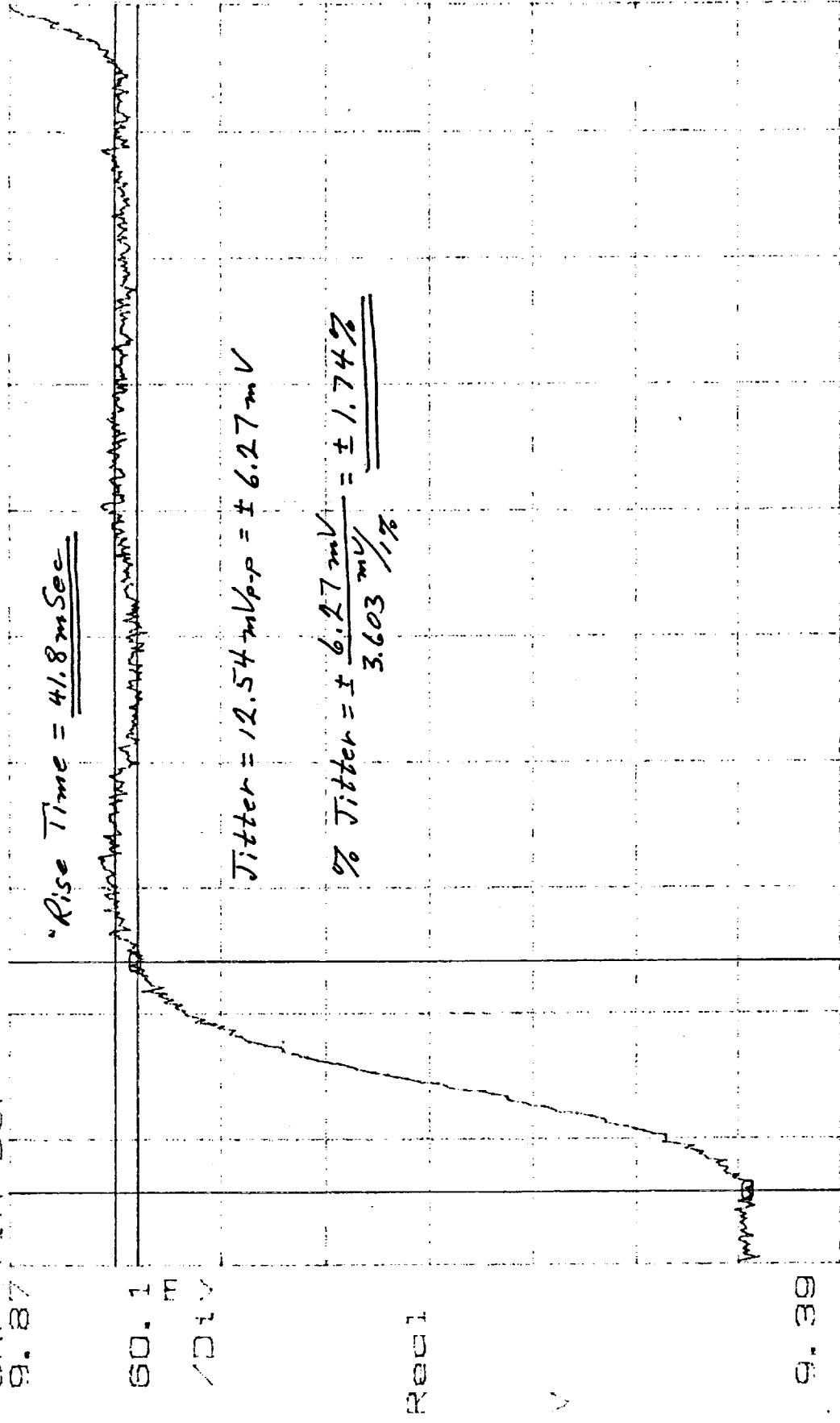
Test Engr: R. Lind Date: 4/20/97

Quality: 268

B/9

X=1.984 S    ΔX=41.8ms    Y=9.79922    ΔY=12.54mV  
Y=9.4455    ΔY=355.2mV

CAP TIM BUF  
9.87



9.39

ENDY 1.97

Sec

7APLFS5

2.2

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

HP 3.4.5.5

Step 10-11

Test Engr: D. Lind Date: 4/20/99

Quality: 99% 4L

B20

Y=9.80622 ΔY=18.07mV

CAP TIM BUF  
9.87

60.1 m  
/DIV

Real

V

9.29

EXDXY 1.97

Sec

7AP.F55

2.2

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IP 3.4.5.5

Step 10-11

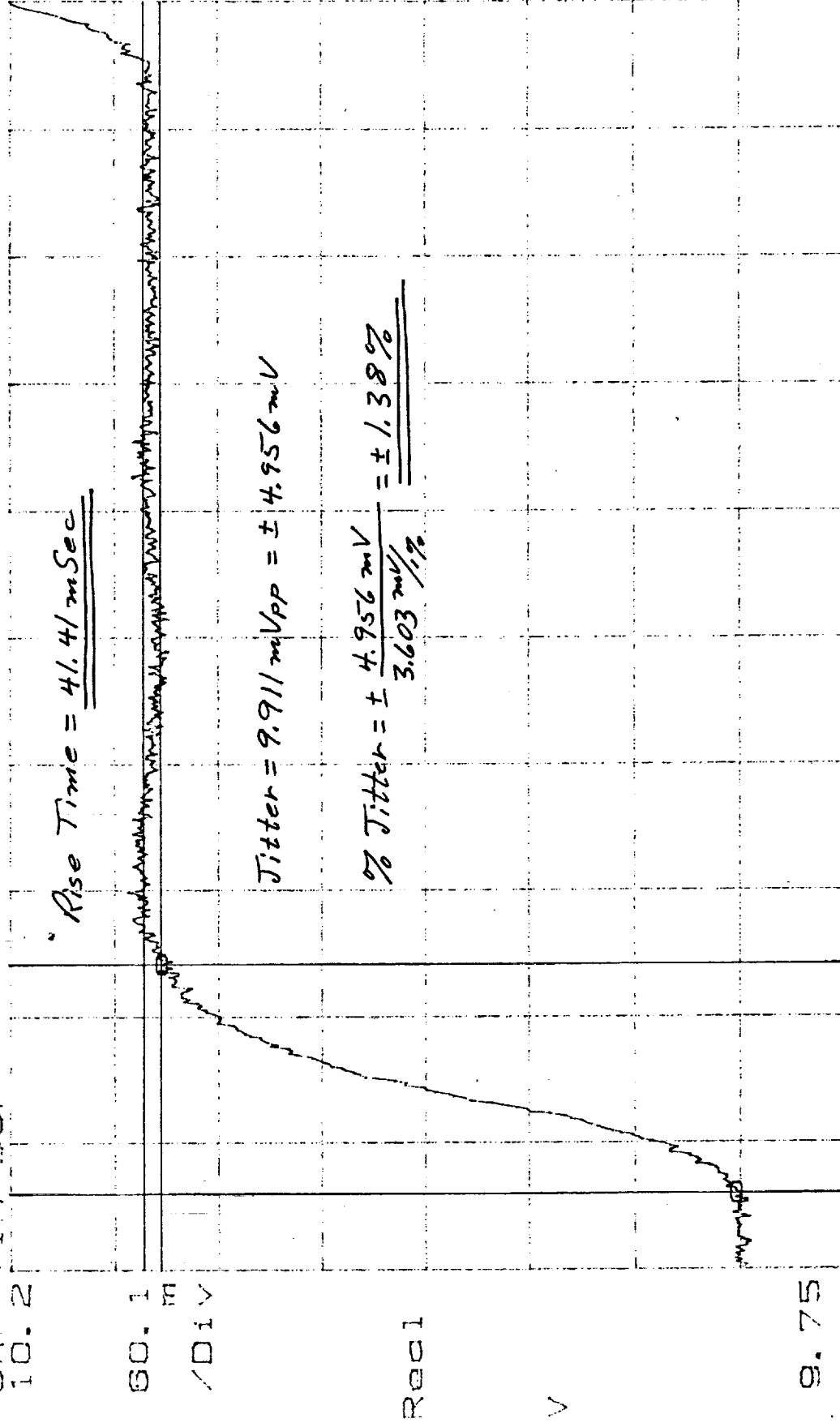
Test Eng: Shad Date: 4/26/97

Quality: 1A

B21

X=2.229 S    ΔX=41.41mS    Y=10.1421    ΔY=9.911mV  
 Y0=10.1413    ΔY0=330.9mV

CAP TIM BUF  
 10.2



ExdXY 2.17 Sec 7AP\_FSS 2.4

S/O: 335168    Scan Motion and Jitter    Test Engr: D. L. L.    Date: 4/20/99  
 P/N: 1331200-2-IT    HP 3.4.5.5    Quality: 89%  
 S/N: 108    Step 11-12

Y=10.1462  $\Delta Y=18.07mV$

CAP TIM BUF  
10.2

60.1 m  
/Div

Real

V

9.75

EXDXY 2.17

Sec

7AP.FS5

2.4

+5% limit

Overshoot = 0.0mV

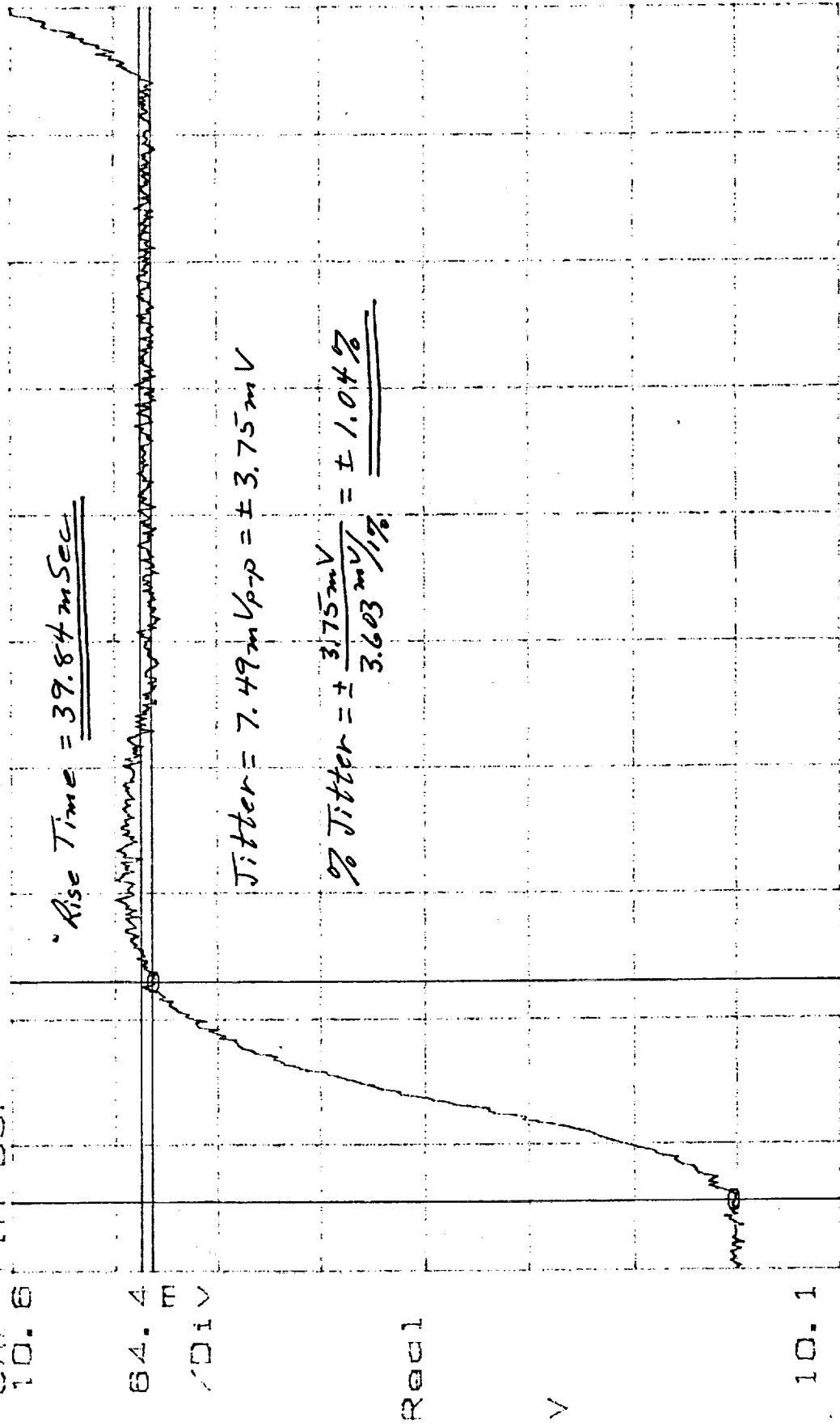
S/O: 335168  
P/N: 1331200-2-IT  
S/N: 108  
Scan Motion and Jitter  
TP 3.4.5.5  
Step 11-12  
Test Engr: Q. S. L. Date: 4/20/92  
Quality: 892 VI

B23

X=2.39 S ΔX=39.84mS Y=10.5175 ΔY=7.49mV

Y0=10.1494 ΔY0=360.0mV

CAP TIM BUF  
10.6



EXDXY 2.38 7AP 155

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IT 3.4.5.5

Step 12-13

Test Engr: D. Smith

Quality: 7A  
260

Date: 4/20/97

B24

Y=10.5119  $\Delta Y=18.1\text{mV}$



Scan Motion and Jitter  
P 3.4.5.5  
Step 12-13

Test Eng: L. S. Ford Date: 4/20/89

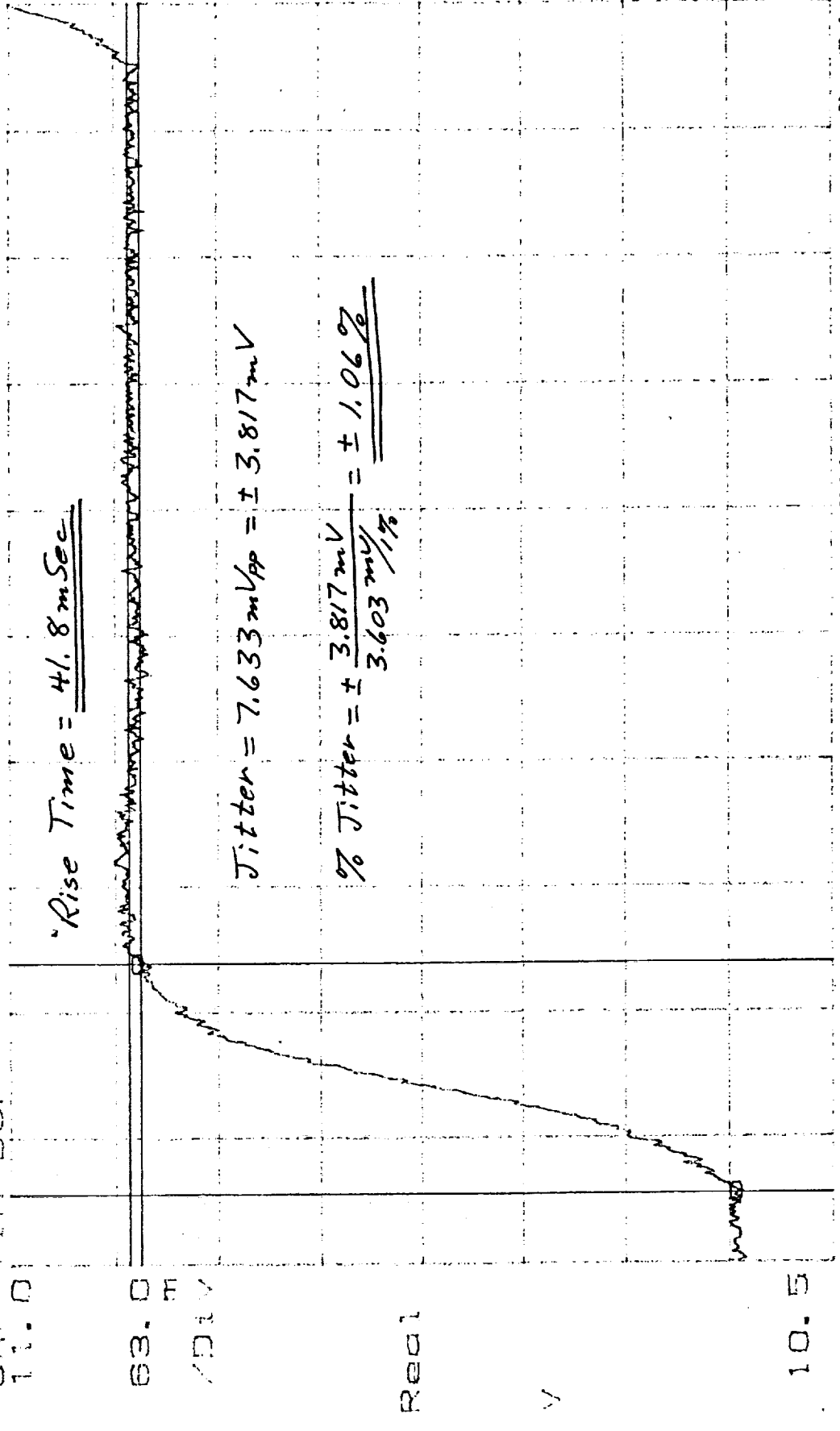
Quality: 1A (258)

S/O: 335168  
P/N: 1331200-2-IT  
S/N: 108

B25

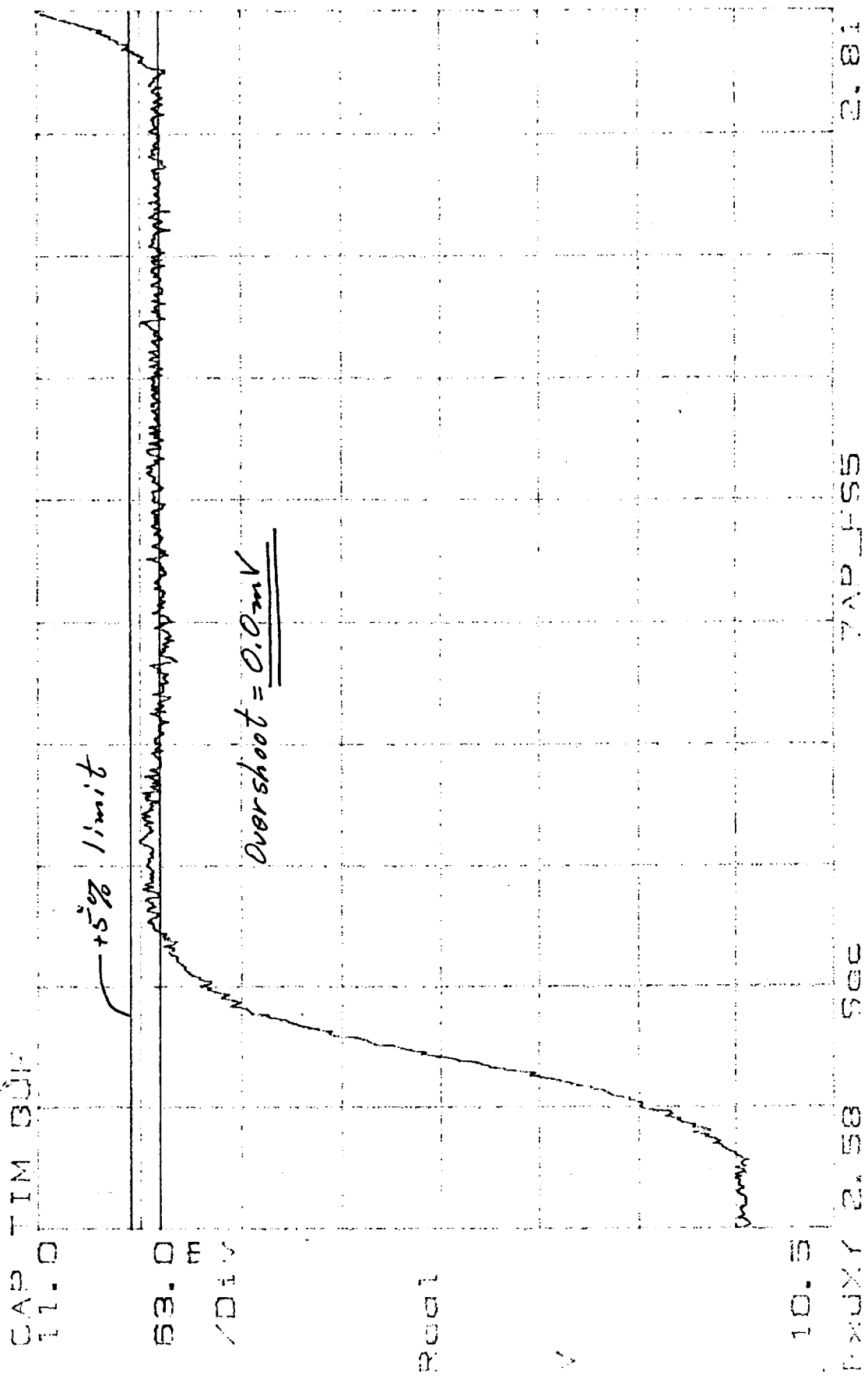
X=2.591 S    ΔX=41.8ms    Y=10.8865    ΔY=7.633mV  
 Y0=10.5127    ΔY0=368.2mV

CAP TIM BUF  
 11.0



S/O: 335168    Scan Motion and Jitter    Test Engr: DeLind    Date: 4/21/69  
 P/N: 1331200-2-IT    R 3.4.5.5    Quality: 89%  
 S/N: 108    Step 13-14

Y=10.8825 ΔY=18.01mV



S/O: 335168  
P/N: 1331200-2-IT  
S/N: 108

Scan Motion and Titter  
P 3.4.5.5  
Step 13-14

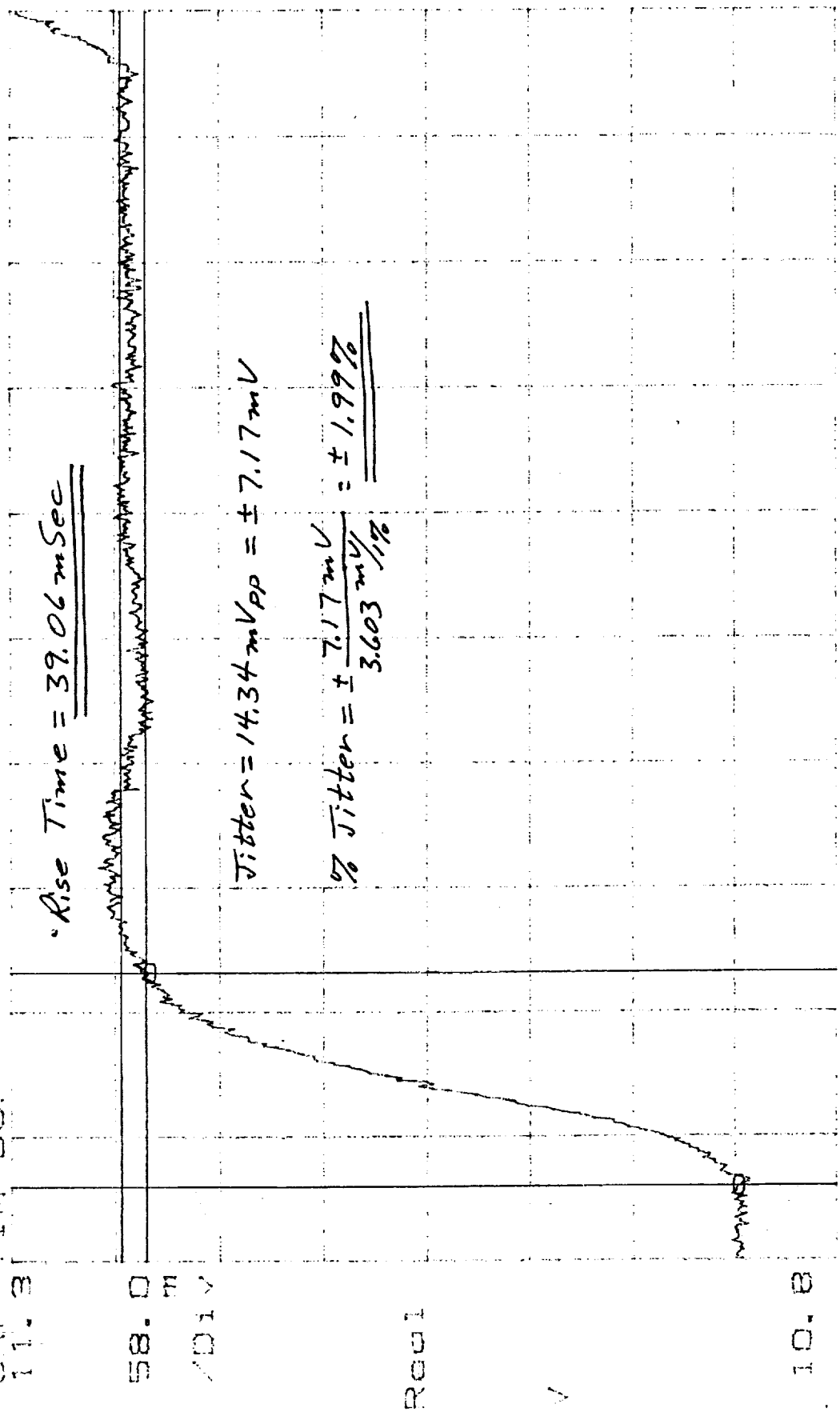
Test Engr: Q. J. J. Date: 4/20/69  
Quality: (7A) 268

7AP J-55 2.81

B27

X=2.834 S    ΔX=39.06ms    Y=11.2315    ΔY=14.34mV  
 Y0=11.2149    ΔY0=330.9mV

CAP TIM BOX  
 11.3



10.8    2.78    3.01    7AP1F55

S/O: 335168    Scan Motion and Jitter    Test Engr: 19 Lead    Date: 4/20/87  
 PH: 1331200-2-IT     $\pi$  3.455    Quality: (268)  
 S/N: 1108    Step 14-15

Y=11.2254 ΔY=18.0mV

CAP TIM BDP  
11.3

58.0

101V

Real

10.8

INDEX 2.78

SEC

7AP.FS5

3.01

+5% limit

Overshoot = 0.0mV

S/b: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IT 3.4.5.5

Step 14-15

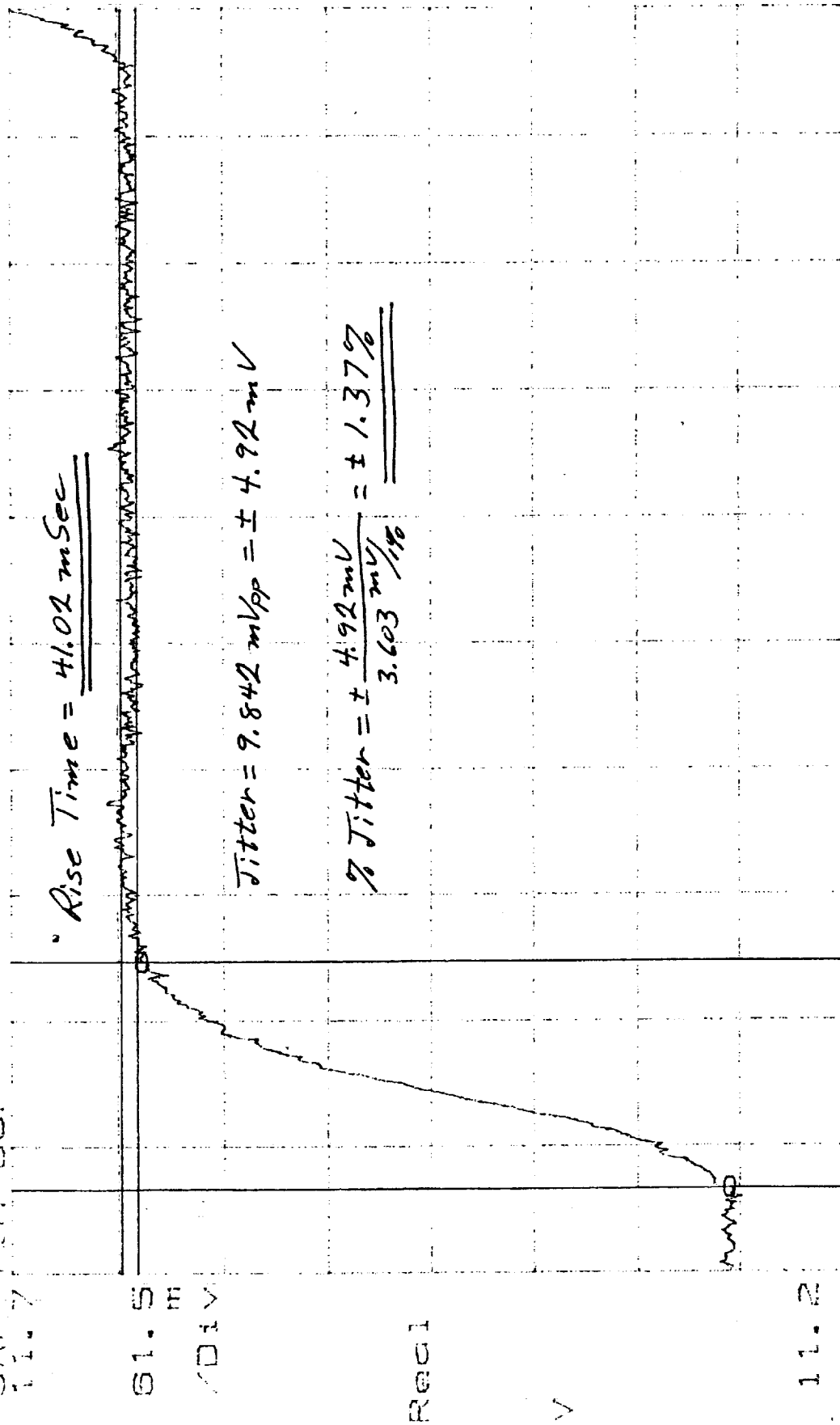
Test Eng: D. Lind Date: 4/20/99

Quality: 7A  
(258)

B29

X=2.999 S ΔX=41.02mS Y=11.5891 ΔY=9.842mV  
 Y=11.2279 ΔY=348.7mV

CAP TIM BUI  
 11.7



11.2

ENDXY 2.98

Sec

7AP F55

3.21

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IT 3.45.5

Step 15-16

Test Engr: D. Lee Date: 4/20/89

Quality: TA 268

B30

Y=11.5828  $\Delta Y=17.89mV$

CAP TIM BUF  
11.7

51.5  
m  
/Div

Real

V

11.2

ENDXY 2.98

Sec

7AP\_F55

3.21

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

TP 3.4, 5.5

Step 15-16

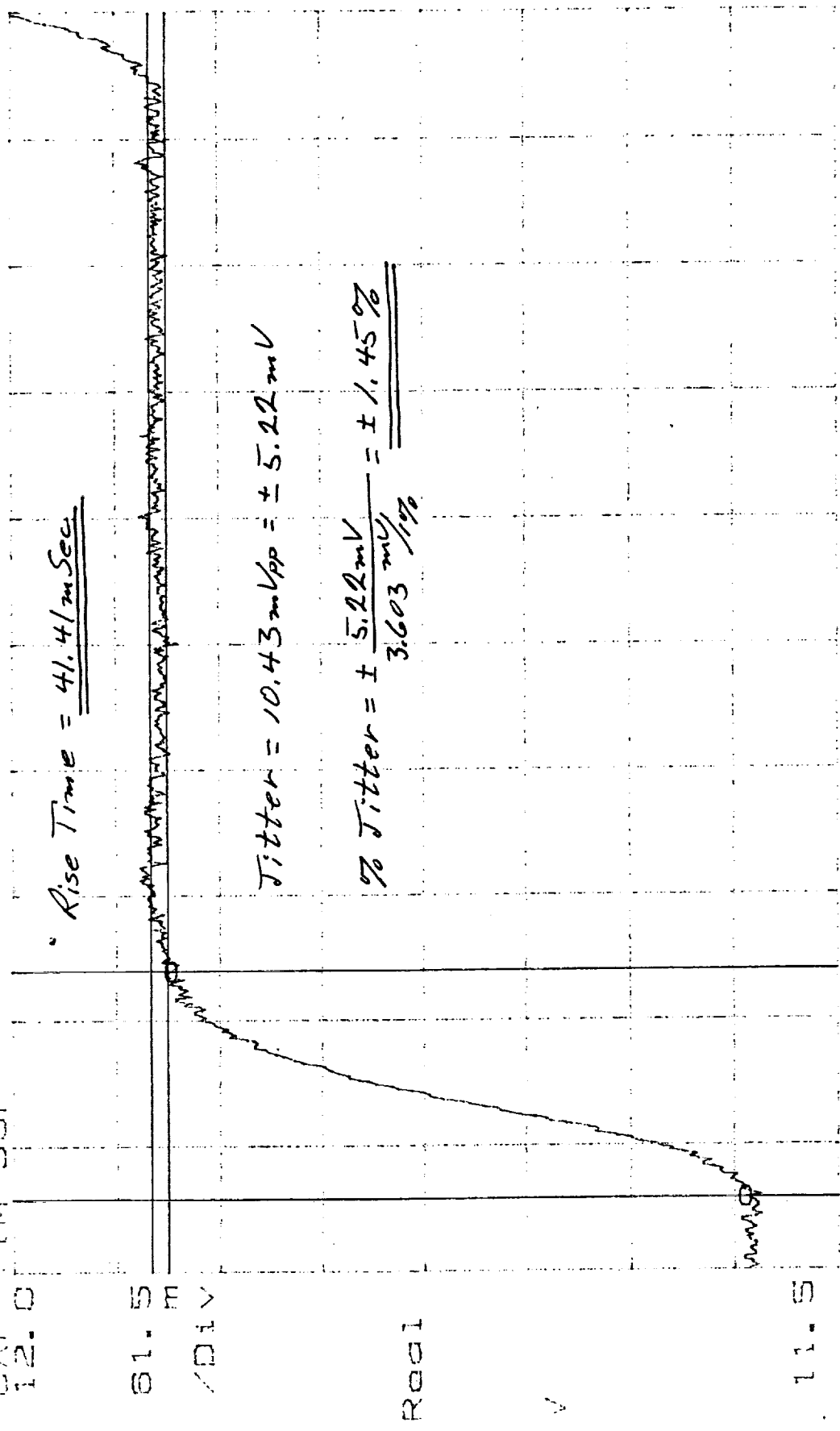
Test Engr: D. Land Date: 4/20/98

Quality: 100%

B31

X=3.242 S    ΔX=41.41mS    Y=11.9444    ΔY=10.43mV  
 Y=11.9318    ΔY=342.2mV

CAP TIM BUF  
 12.0



11.5

EXDXY 3.19

Sec

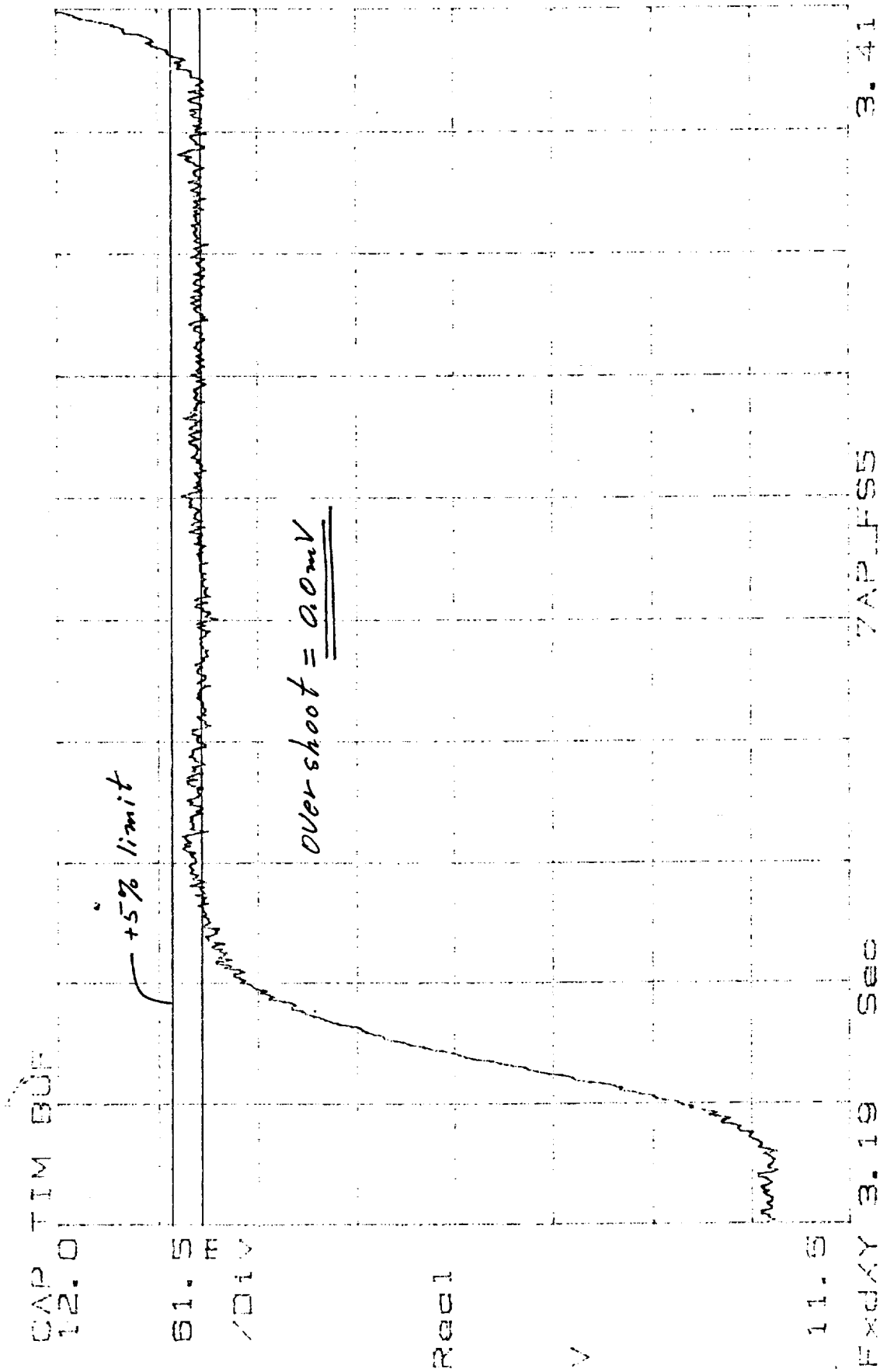
7AP.FS5

3.41

S/O: 335168    Scan Motion and Jitter    Test Engr: Q. Land    Date: 4/20/99  
 P/N: 1331200-2-IT    T 3.45.5    Quality: 100%  
 S/N: 108    Step 16-17

B32

Y=11.9381  $\Delta Y=18.18mV$



S/O: 335/68  
P/N: 1331200-2-IT  
S/N: 108

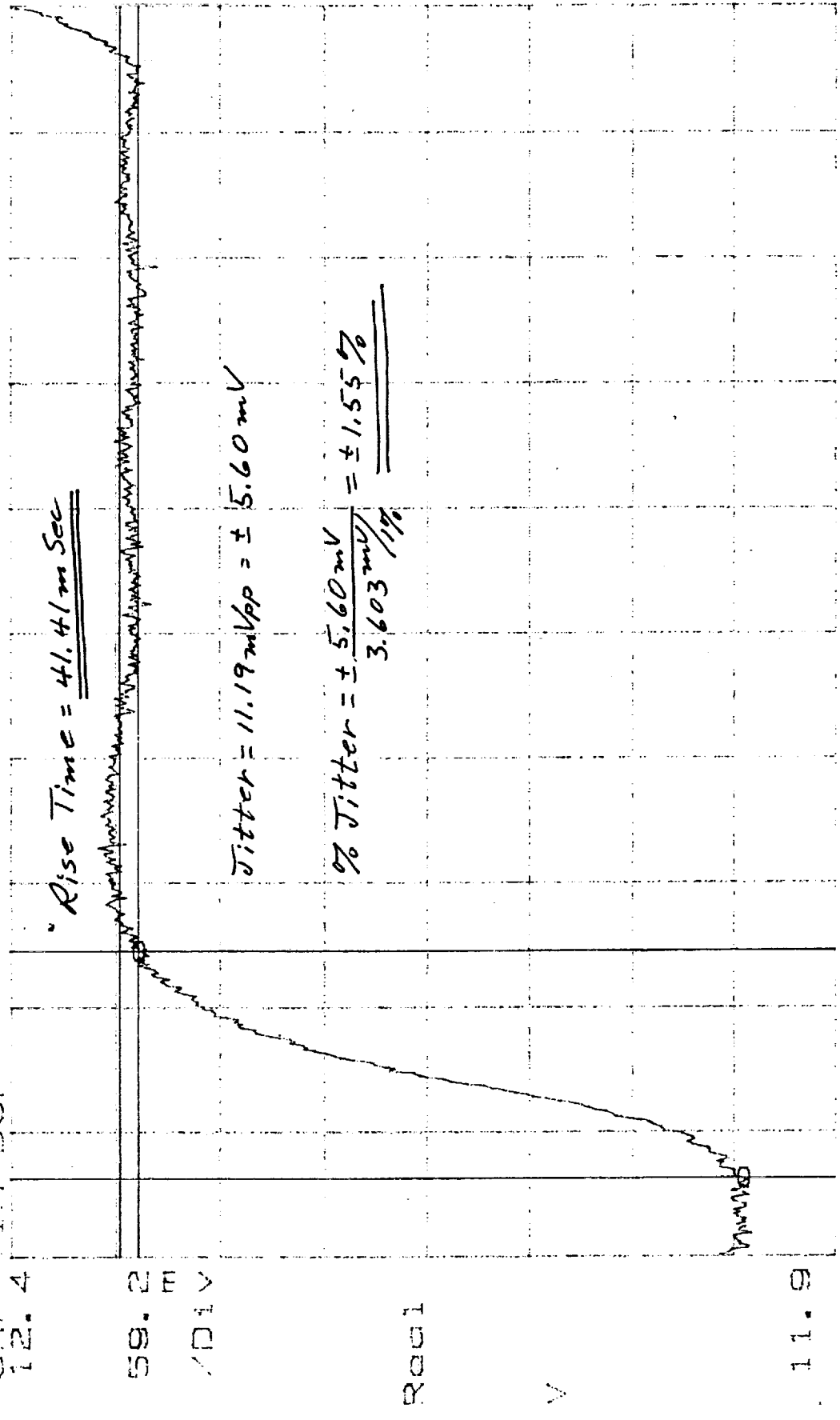
Scan Motion and Jitter  
P 3.4.5.5  
Step 16-17

Test Engr: DeLud Date: 4/20/89  
Quality: (89%)

B33

X=3.402 S      ΔX=41.41ms      Y=12.297      ΔY=11.19mV  
 YQ=11.9382      ΔYQ=347.1mV

CAP TIM BUF  
 12.4



11.9

FXDXY 3.39

Sec

7APJ-S5

3.62

S/O: 335168

PN: 1331200-2-IT

SN: 108

Scan Motion and Jitter

π 3.4.5.5

Step 17-18

Test Engr: D. L. L. Date: 4/21/99

Quality: TA 258

B34

Y=12.2898  $\Delta Y=18.07mV$

CAP TIM BUE  
12.4

59.2E  
/DIV

Real

V

11.9

EXDXY 3.39

Sec

7AP F55

3.62

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

TP 3.4.5.5

Step 17-18

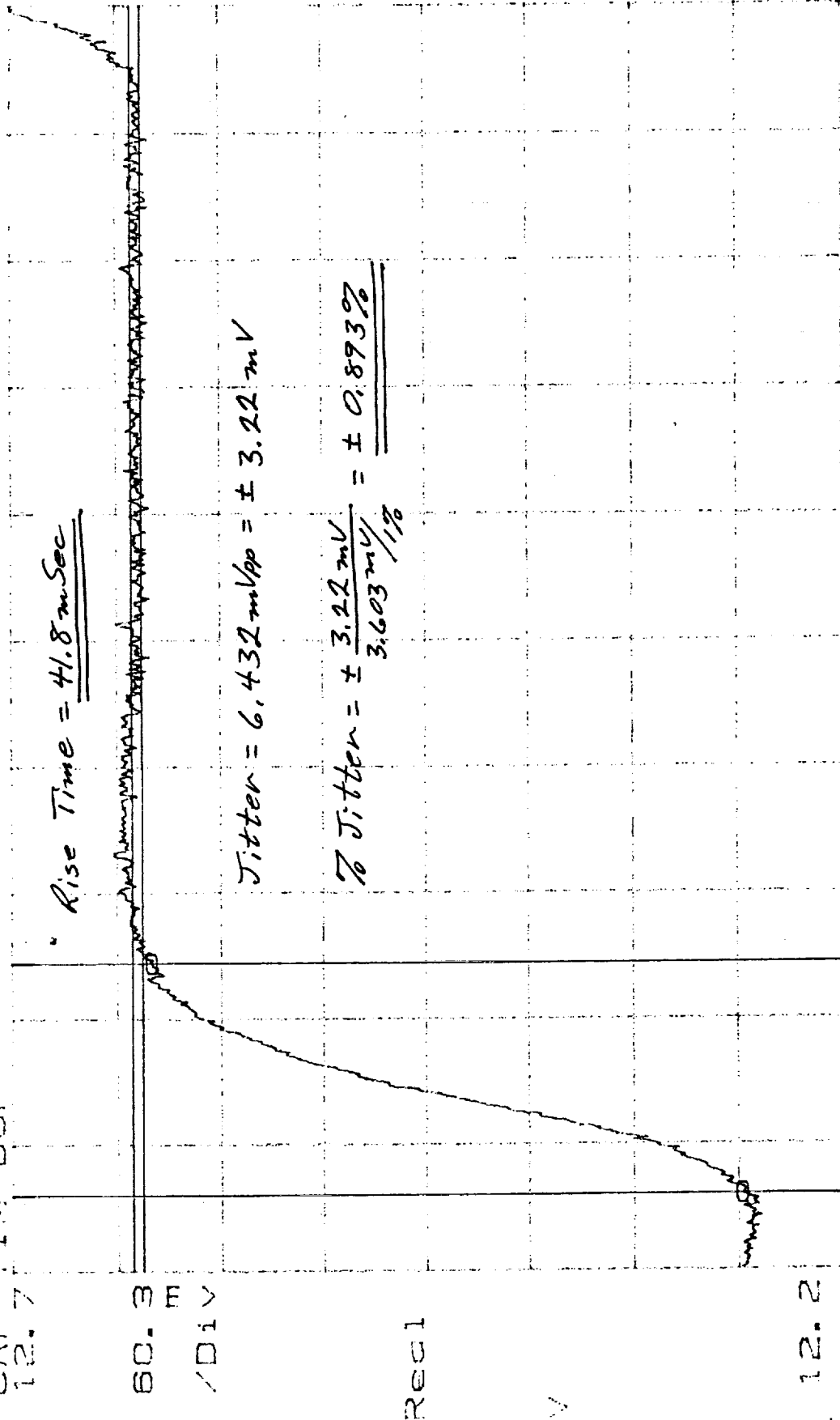
Test Eng: Quinlan Date: 4/21/99

Quality: (14/280)

B35

X=3.605 S    ΔX=41.8mS    Y=12.6492    ΔY=6.432mV  
 Y=12.2934    ΔY=343.8mV

CAP TIM BUF  
 12.7



60.3 m  
 /Div

Rec1

12.2

EXDXY 3.59

Sec

7AP F55

3.82

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

3.4.5.5

Step 18-19

Test Eng: D. Lind Date: 4/21/99

Quality: 1A

B36

Y=12.6454     $\Delta Y=17.82\text{mV}$

CAP TIM BUF  
12.7

60.3  
H  
/DIV

Real

12.2

PKDLY 3.59

SEC

VAP, P55

3.82

+5% limit

Overshoot = 0.0mV

S/O: 335/68

PN: 1331200-2-IT

SN: 108

Scan Motion and Jitter  
PI 3.4.5.5

Step 18-19

Test Engr: D. L. L.

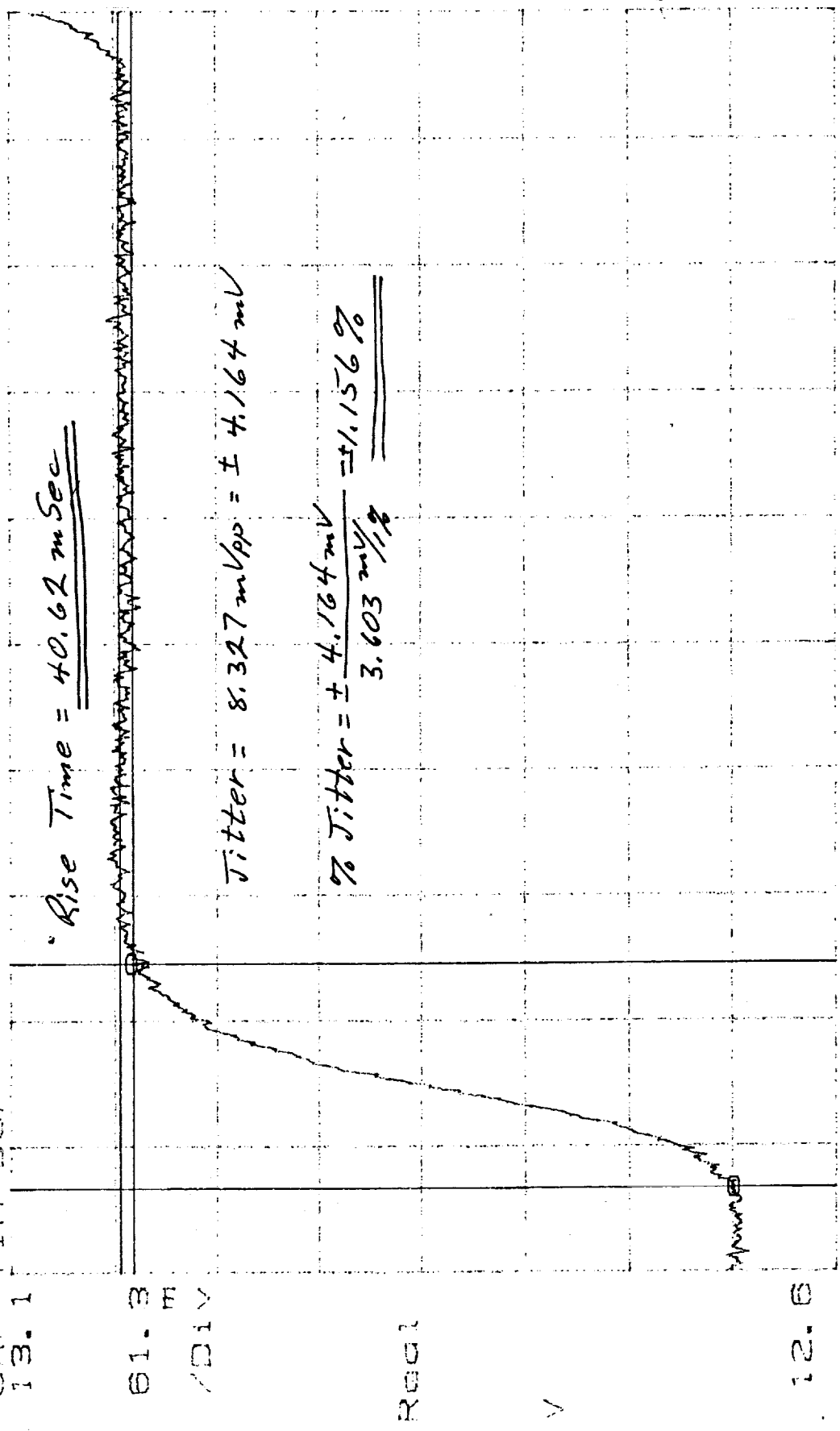
Date: 4/21/99

Quality: TA  
(268)

B37

X=3.807 S    ΔX=40.62ms    Y=13.0128    ΔY=8.327mV  
 Y0=12.647    ΔY0=358.4mV

CAP TIM BUF  
 13.1



12.6    7AP.F95    4.02  
 Fixdxy 3.79    Scc

S/O: 335168    Scan Motion and Jitter    Test Engr: Paul    Date: 4/21/88  
 P/W: 1331200-2-IT    7 3.45.5    Quality: (99%)  
 S/N: 108    Step 19-20    B38

Y=13.0083 ΔY=18.14mV

GAP TIM 3J4  
13.1

+5% limit

01.3  
DIV

Overshoot = 0.0mV

Reol

V

12.6

FXDXY 3.79

Sao

7AP1F55

4.02

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter  
RP 3.4.5.5

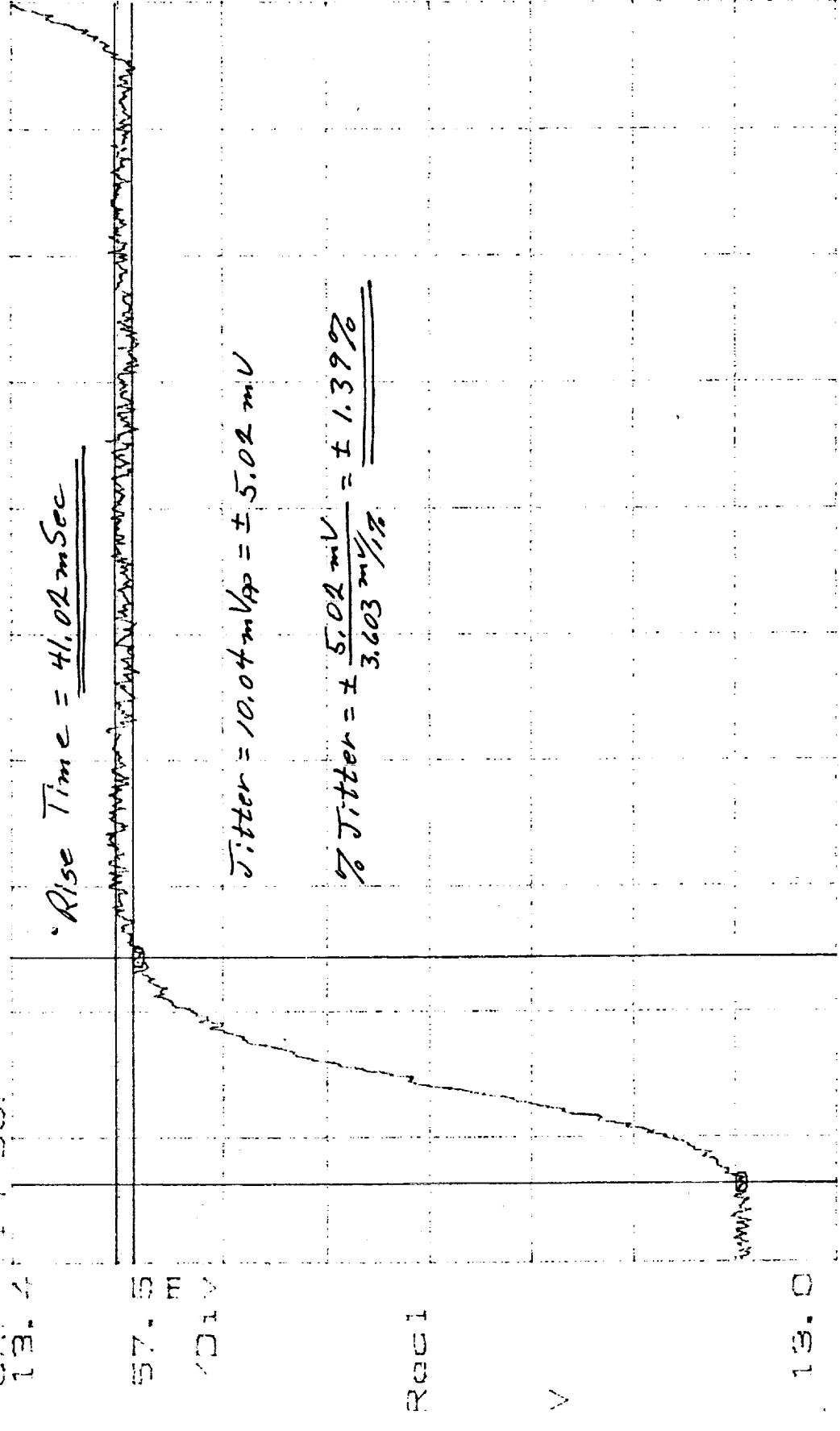
Step 19-20

Test Engr: Q. Lead Date: 4/24/99  
Quality: (VI)

B39

X=4.011 S      ΔX=41.02ms      Y=13.3607      ΔY=10.04mV  
 Y=13.0103      ΔY=337.3mV

CAP TIM B01  
 13.4



13.0      4.0      4.22  
 PEXXY      SSEC      VAP, PSE

No: 335168      Scan Motion and Jitter      Test Engr: Ded      Date: 4/21/99  
 PH: 1331200-2-IT      H 34.5.5      Quality: 99%  
 SW: 108      step 20-21      B40

Y=13.3545 ΔY=18.13mV

CAP TIM BUN  
13.4

57.5 m  
Div

Real

13.0

FIXDXY 4.0

Sec

ZAP J.S5

6.22

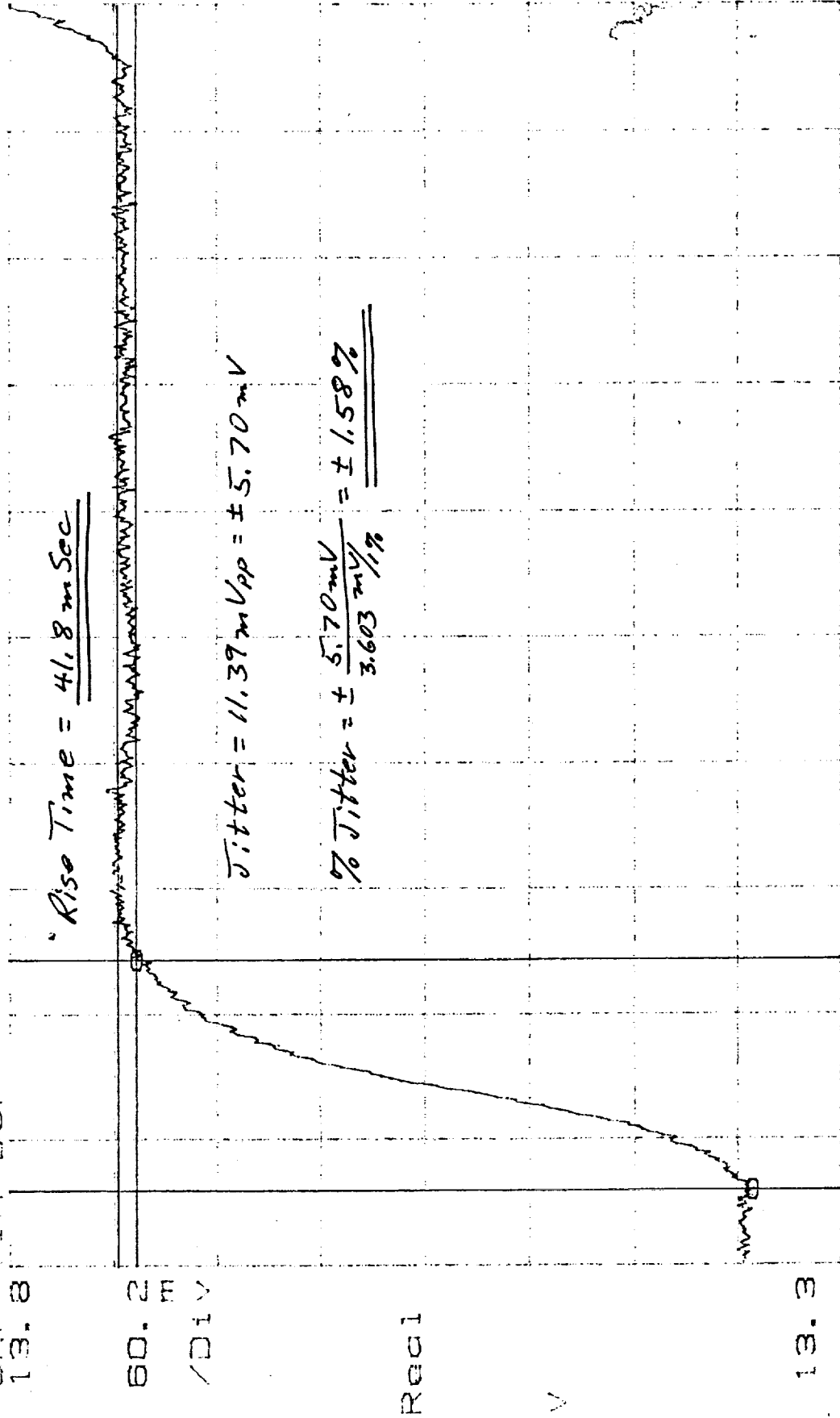
+5% limit

Overshoot = 0.0mV

S/N: 335168  
P/N: 1331200-2-IT  
S/N: 108  
Scan Motion and Jitter  
IF 3.4.5.5  
Step 20-21  
Test Engr: Deled Date: 4/2/82  
Quality: (89% VL)  
B41

X=4.212 S    ΔX=41.8ms    Y=13.7174    ΔY=11.39mV  
 Y=13.3492    ΔY=356.8mV

CAP TIM BUF  
 13.8



13.3

INDEXY 4.2

SEC

7AP.FS5

4.43

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IP 3.4.5.5

step 21-22

Test Engr: D. L. L. Date: 4/2/88

Quality: (A)

B42

Y=13.7114 ΔY=17.98mV

CAP TIM BUF  
13.8

59.8 m  
V DIV

Real

13.3

PXIXY 4.2 SEC

7AP F:55

4.43

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IT 3.45.5

Step 21-22

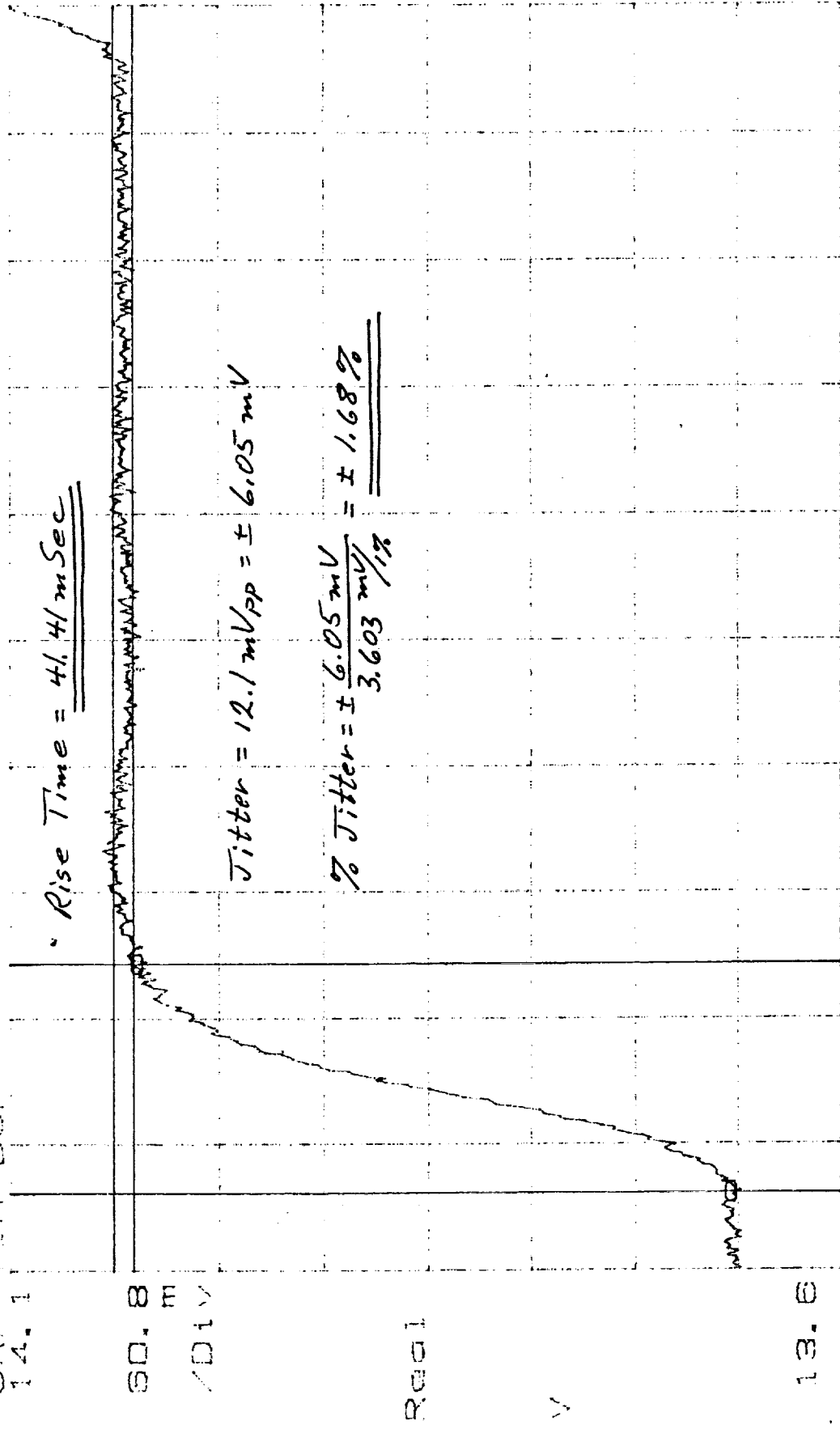
Test Eng: D. L. L. Date: 4/24/87

Quality: (298) 44

B43

X=4.456 S    ΔX=41.41mS    Y=14.0755    ΔY=12.1mV  
 Y0=14.0612    ΔY0=347.1mV

CAP TIM BUF  
 14.1



ExdY 4.4 Sec

7AP\_FS5

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter Test Engr: D. Lind Date: 4/21/88

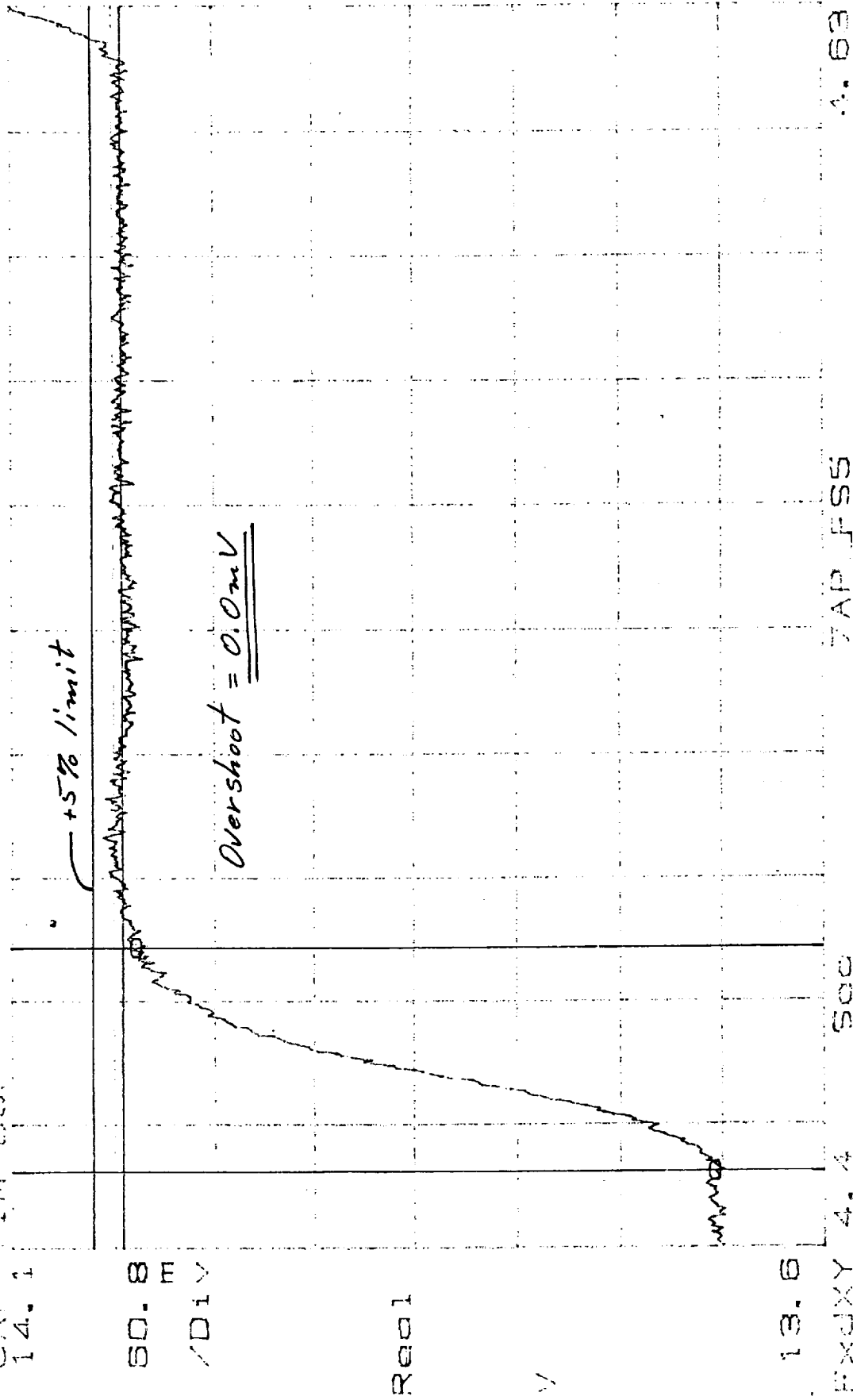
Quality: (14.68)

Step 22-23

B44

X=4.456 S    ΔX=41.41 mS    Y=14.0696    ΔY=18.0 mV  
 YQ=14.0612    ΔYQ=347.1 mV

CAP TIM BOUT  
 14.1

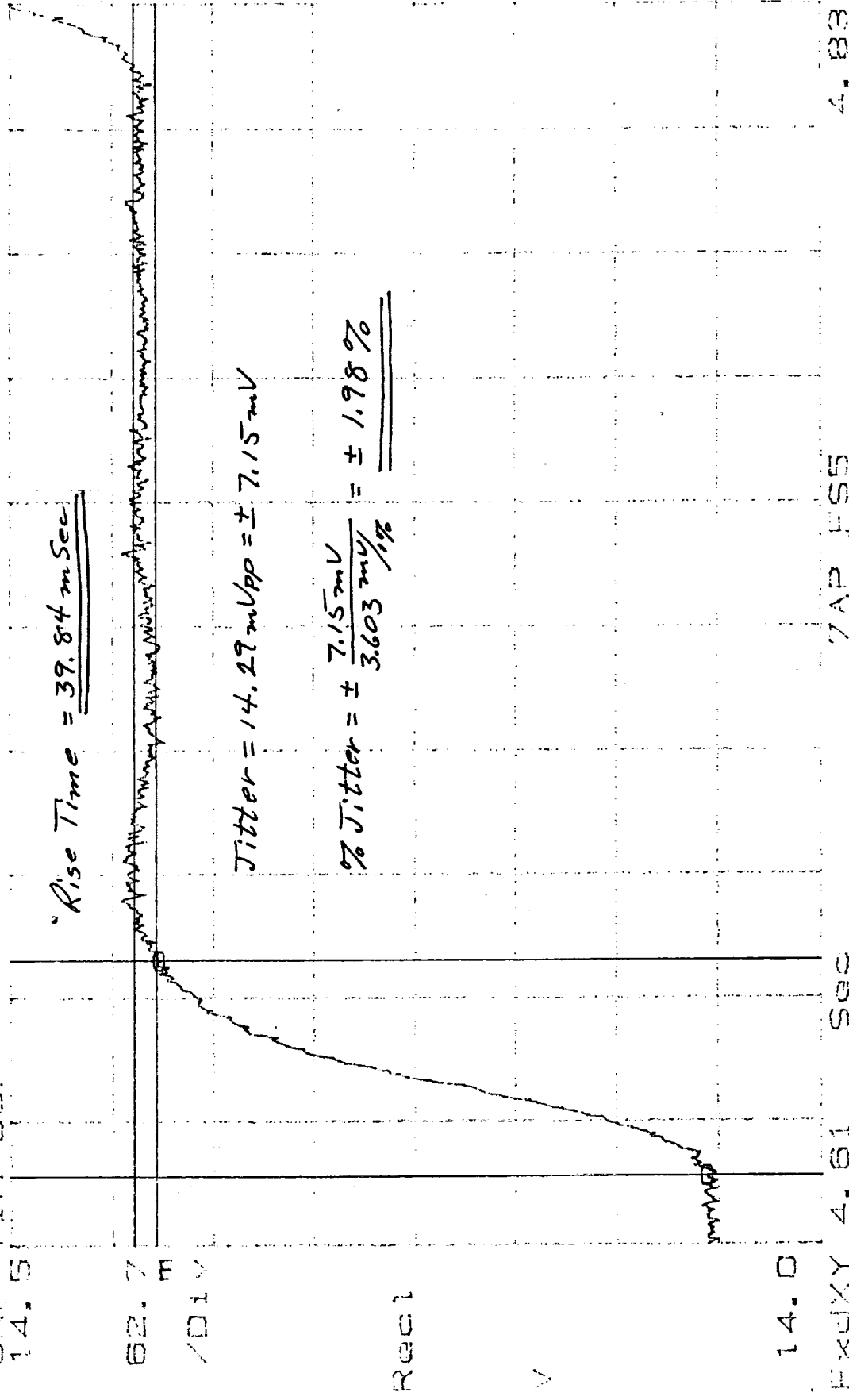


S/O: 335168    Scan Motion and Jitter    Test Engr: David    Date: 4/2/82  
 P/N: 1331200-2-IT    3.45.5    Quality: 1A  
 S/N: 108    Step 22-23    B45

X=4.657 S ΔX=39.84ms Y=14.4291 ΔY=14.29mV

Y=14.4131 ΔY=340.6mV

CAP TIM 601  
14.5



14.0

FRUXY 4.61

Sec

7AP FS5

4.83

S/O: 335168

PN: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

TP 3.4.5.5

Step 23-24

Test Engr: Q. L. L.

Date: 4/2/68

Quality: 1A  
260

B46

Y=14. 4221  $\Delta Y=17.94mV$

CAP TIM BUF  
14.5

62.7  
m  
/DIV

Rec1

14.0

FXDXY 4.61 Sec

Sec

7AP.F55

4.83

+5% limit

Overshoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

TP 3.4.5.5  
Step 23-24

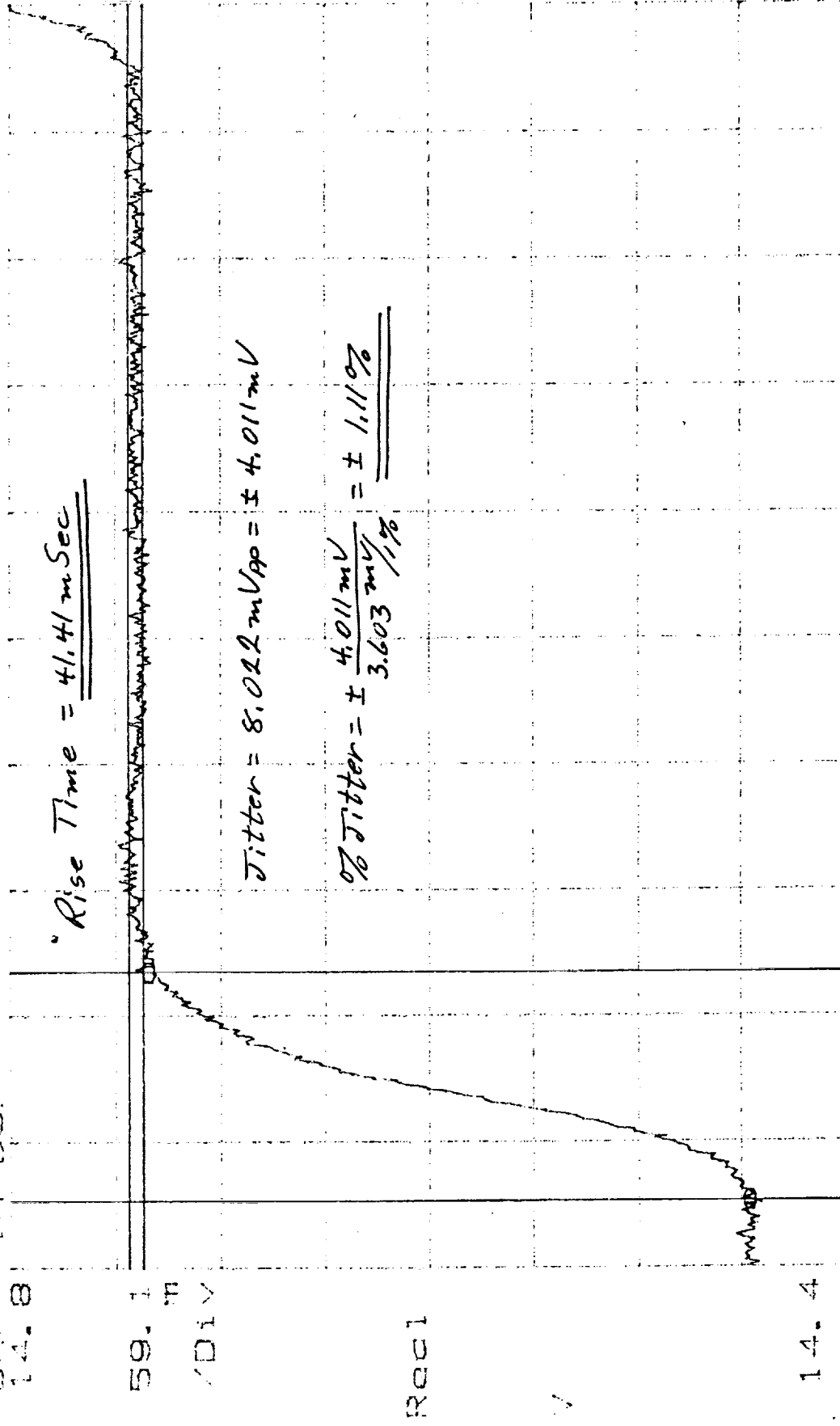
Test Engr: D. L. L. Date: 4/21/88

Quality: 1A  
958

B47

X=4.86 S ΔX=41.41mS Y=14.7683 ΔY=8.022mV  
Y=14.7651 ΔY=340.6mV

CAP TIM BUI-  
14.8



14.4

14.81 SEC

59.03

7AP JES5

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

TP 3.4.5.5

Step 24-25

Test Engr: David

Date: 4/2/98

Quality: 1A

B48

Y=14.7715  $\Delta Y=18.05mV$

CAP TIM B3.17  
14.8

+5% limit

59.1

F

VDIV

overshoot = 0.0mV

RG01

14.4

PADXY 4.81

Sec

7AP JFS5

5.03

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

IP 3.4.5.5

step 24-25

Test Engr: D. L. L.

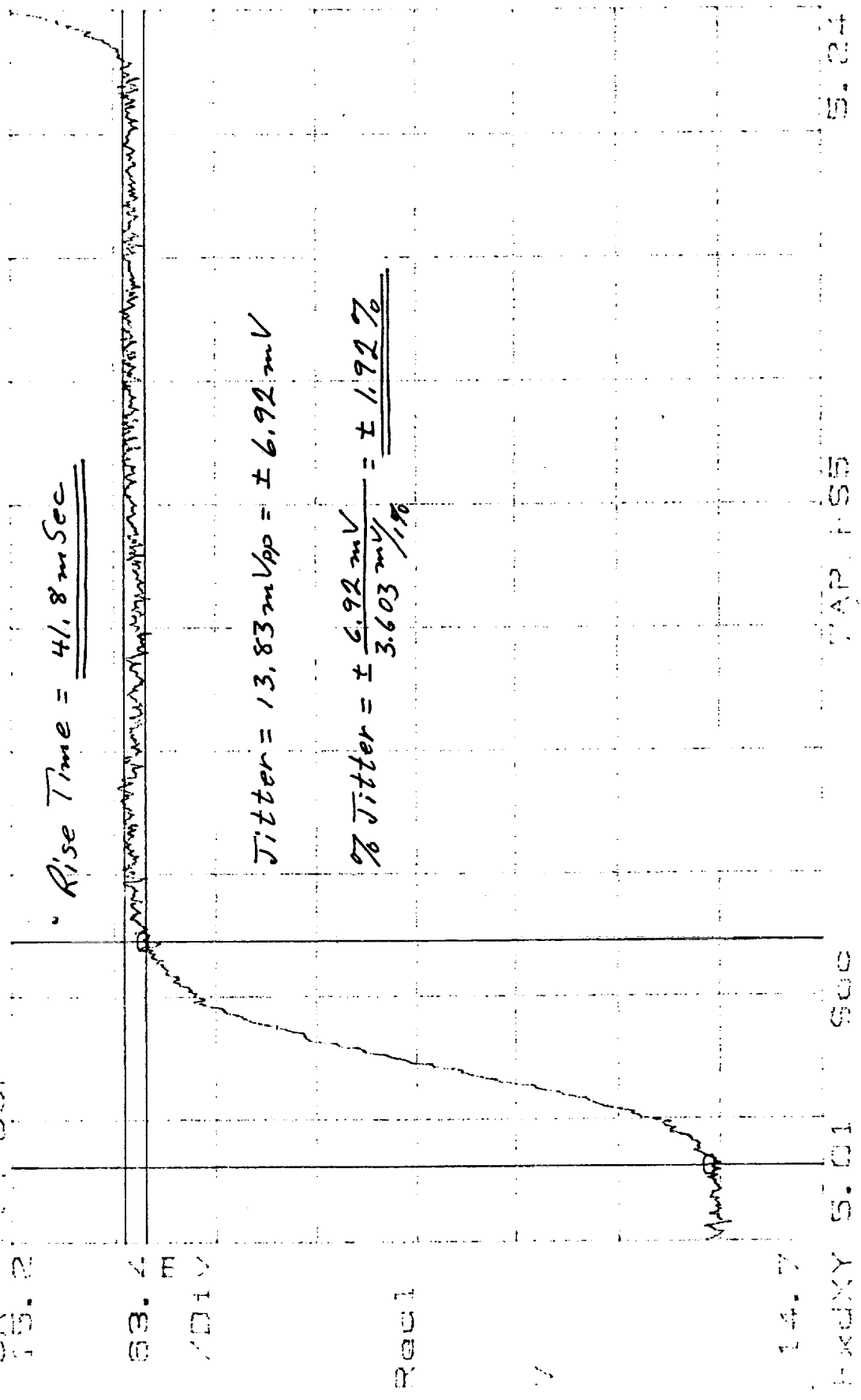
Date: 4/21/99

Quality: (7A) (260)

849

X=5.064 S      ΔX=41.8mS      Y=15.1439      ΔY=13.83mV  
 Yα=15.1316      ΔYα=355.2mV

CAP TIM 90F  
 15.2



S/O: 335168      Scan Motion and Jitter      Test Engr: D. Lind      Date: 4/1/89  
 PN: 1331200-2-IT      P 3.4.5.5      Quality: 7A  
 SN: 1108      Step 25-26      269  
 B50

Y=15.1377  $\Delta Y=18.13mV$

CAP TIM BUI  
15.2

+5% limit

53.4  
m  
/DIV

Over shoot = 0.02mV

Real

14.7

EXDXY 5.01

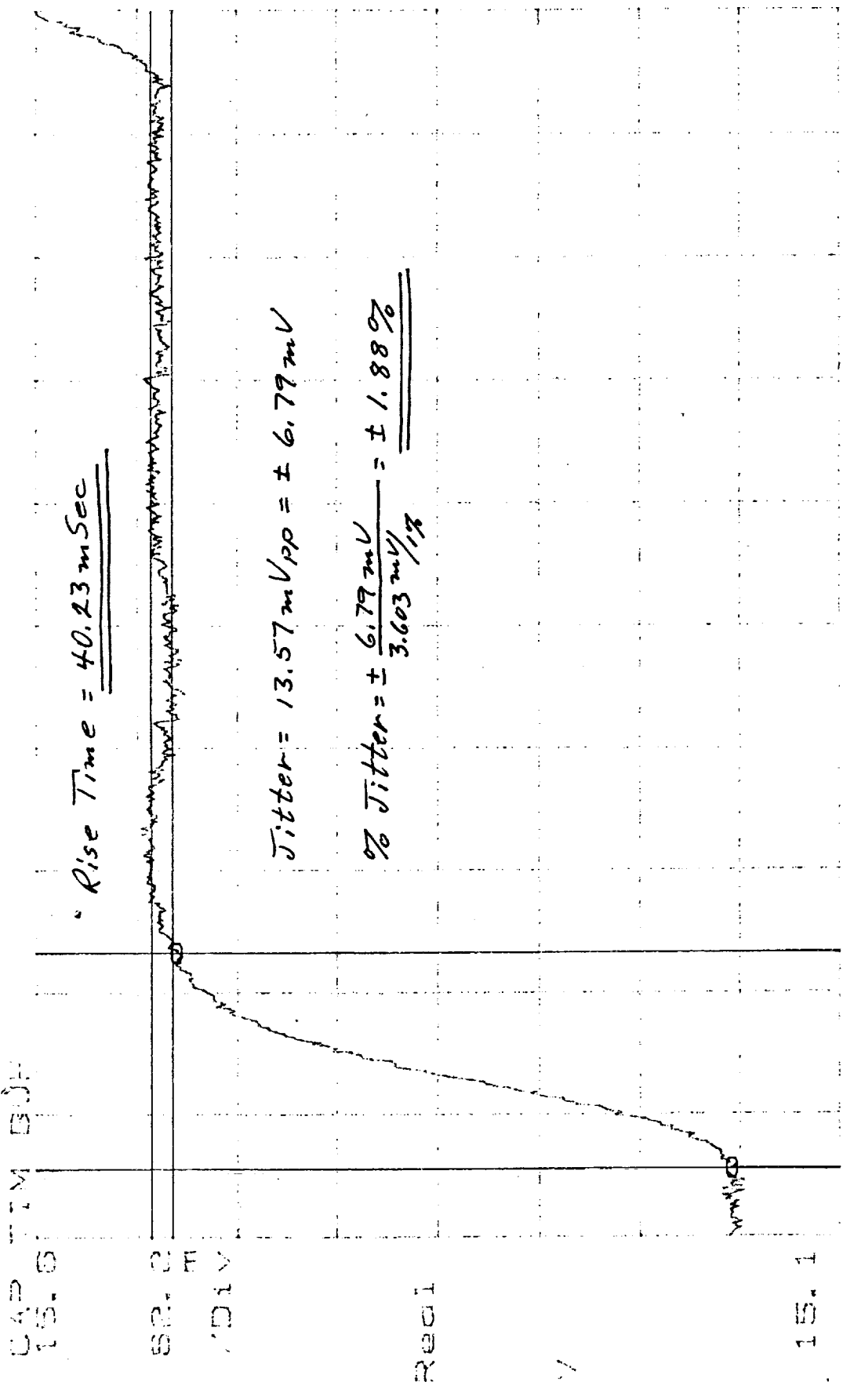
Sec

7AP1555

5.24

S/O: 335168 Scan Motion and Jitter Test Engr: D. Sed Date: 4/4/92  
P/N: 1331200-2-IT HF 3.4.5.5 Quality: 7A  
S/N: 108 Step 25-26 268 BS1

X=5.226 S      ΔX=40.23mS      Y=15.4997      ΔY=13.57mV  
 Y=15.1397      ΔY=343.8mV



S/0: 335/68      Scan Motion and Jitter      Test Engr: D. Lind      Date: 4/2/88  
 P/N: 1331200-2-JT      R 3.4.5.5      Quality: (268)  
 S/N: 108      Step 26-27      B52

Y=15.4944  $\Delta Y=18.12\text{mV}$

CAP TIM BOF  
15.6

+5% limit

62.3

ADIV

Real

V

15.1

EXDXY 5.21

SAC

7AP F55

5.44

Over-shoot = 0.0mV

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

HP 3.4.5.5

step 26-27

Test Engr: D. L. L.

Quality: (268)

Date: 4/2/82

B53

X=5.427 S    ΔX=41.8mS    Y=15.8539    ΔY=10.58mV  
 Y0=15.4933    ΔY0=348.7mV

CAP TIM BUF  
 15.9

Rise Time = 41.8mSec

62.4 m

/DIV

Jitter = 10.58mVpp = ± 5.29mV

% Jitter =  $\pm \frac{5.29mV}{3.603mV/\%} = \pm 1.47\%$

Real

15.4

PROXY 5.41

Sec

7AP.FS5

5.64

S/O: 335168    Scan Motion and Jitter    Test Engr: D. Lind    Date: 4/2/98  
 P/N: 1331200-2-IT    H 3.4.5.5    Quality: (74) (288)  
 S/N: 108    step 27-28

B54

Y=15.8476  $\Delta Y=18.14mV$

CAP TIM BUF  
15.9

62.4  
m

/DIV

Real

15.4

EXDXY 5.41

Sec

7AP\_FFS

5.64

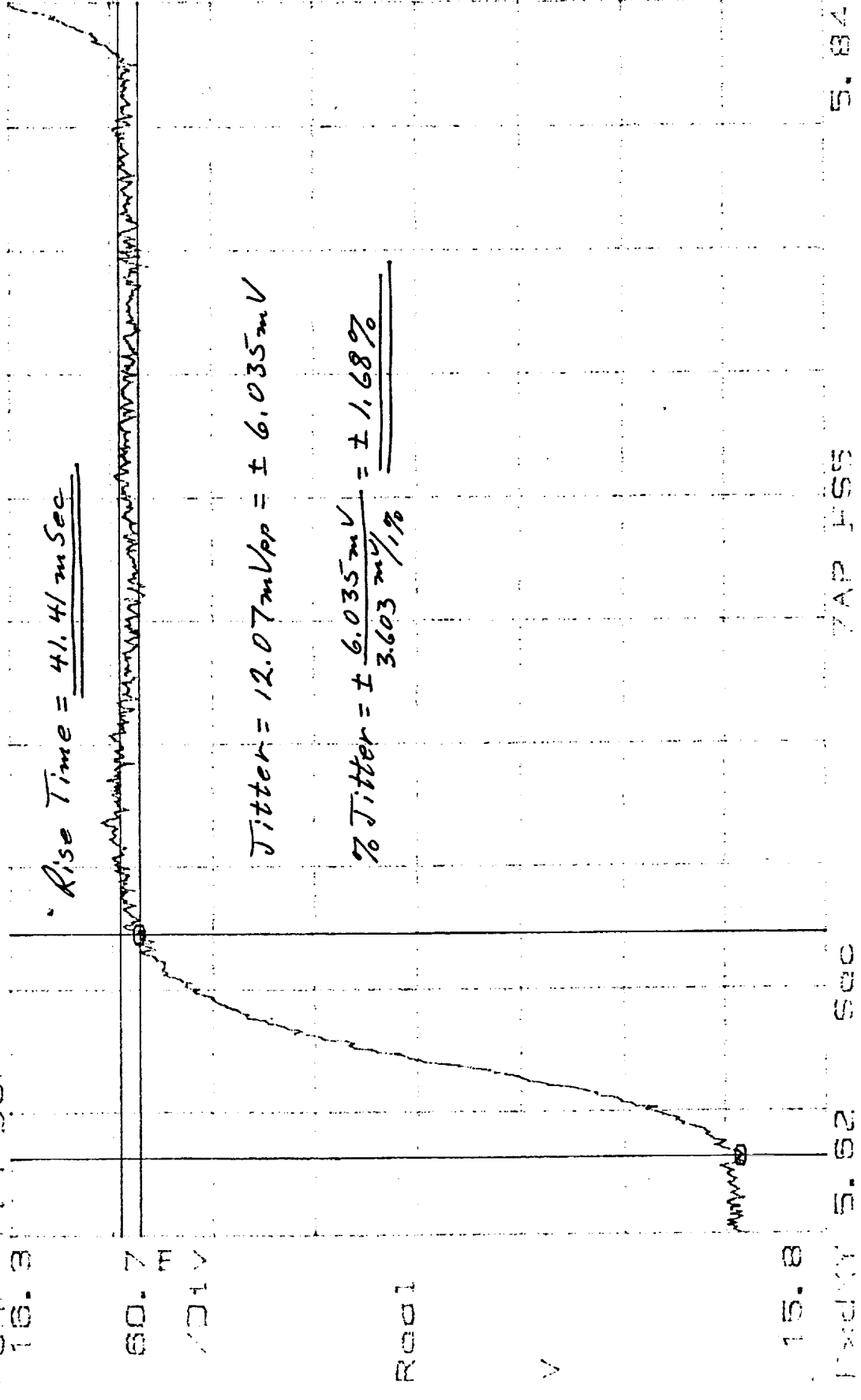
+5% limit

Overshoot = 0.0mV

S/b: 335168 Scan Motion and Jitter Test Eng'n: Daniel Date: 4/21/99  
P/N: 1331200-2-IT Quality: (268)  
S/N: 108 Step 27-28 B55

X=5.63 S      ΔX=41.41 mS      Y=16.2153      ΔY=12.07 mV  
 Y0=15.8468      ΔY0=356.8 mV

CAP TTN 30+  
 16.3



S/N: 335168      Scan Motion and Jitter      Test Eng: D. Lind      Date: 4/6/92  
 P/N: 1331200-2-IT      IF 3.45.5      Quality: (7A 268)  
 S/N: 108      Step 28-29

Y=16.2082  $\Delta Y=17.96\text{mV}$

CAP TIM BUF  
16.3

+5% limit

50.7

m

1010

Overshoot = 0.0mV

Rec1

15.8

EXDXY 5.82

Sec

7AP F55

5.84

S/O: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

F 3.4.5.5

step 28-29

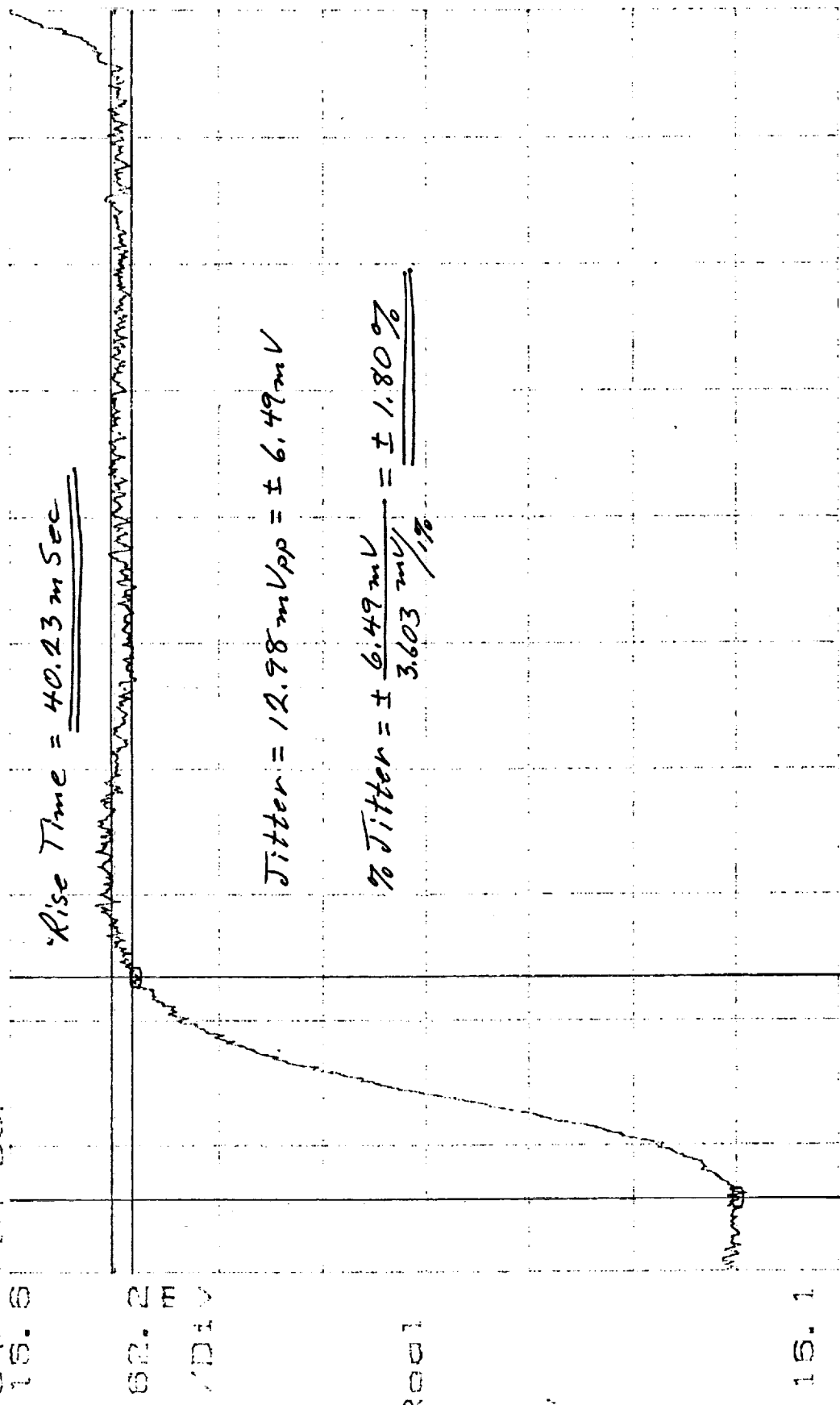
Test Engr: D. L. L. Date: 4/2/92

Quality: 7A  
(888)

B57

X=5.832 S    ΔX=40.23mS    Y=16.5853    ΔY=12.98mV  
 Y=16.2085    ΔY=361.7mV

CAP TIM BOP  
 15.6



EXJY 5.82    7AP J 55    6.05

S/N: 335168    Scan Motion and Jitter  
 P/N: 1331200-2-IT    F 3.4.5.5  
 S/N: 108    Step 29-30

Test Engn: D. L. Ladd    Date: 4/2/88  
 Quality: (74/258)

Y=16.5783  $\Delta Y=18.1\text{mV}$

0.46.0  
1.21.0

±5% limit

0.2.0  
0.4.0

Overshoot = 0.0mV

0.001

16.1

0.46.0  
1.21.0

0.001

0.46.0  
1.21.0

0.001

S/N: 335168

P/N: 1331200-2-IT

S/N: 108

Scan Motion and Jitter

FF 3.4.5.5

Step 29-30

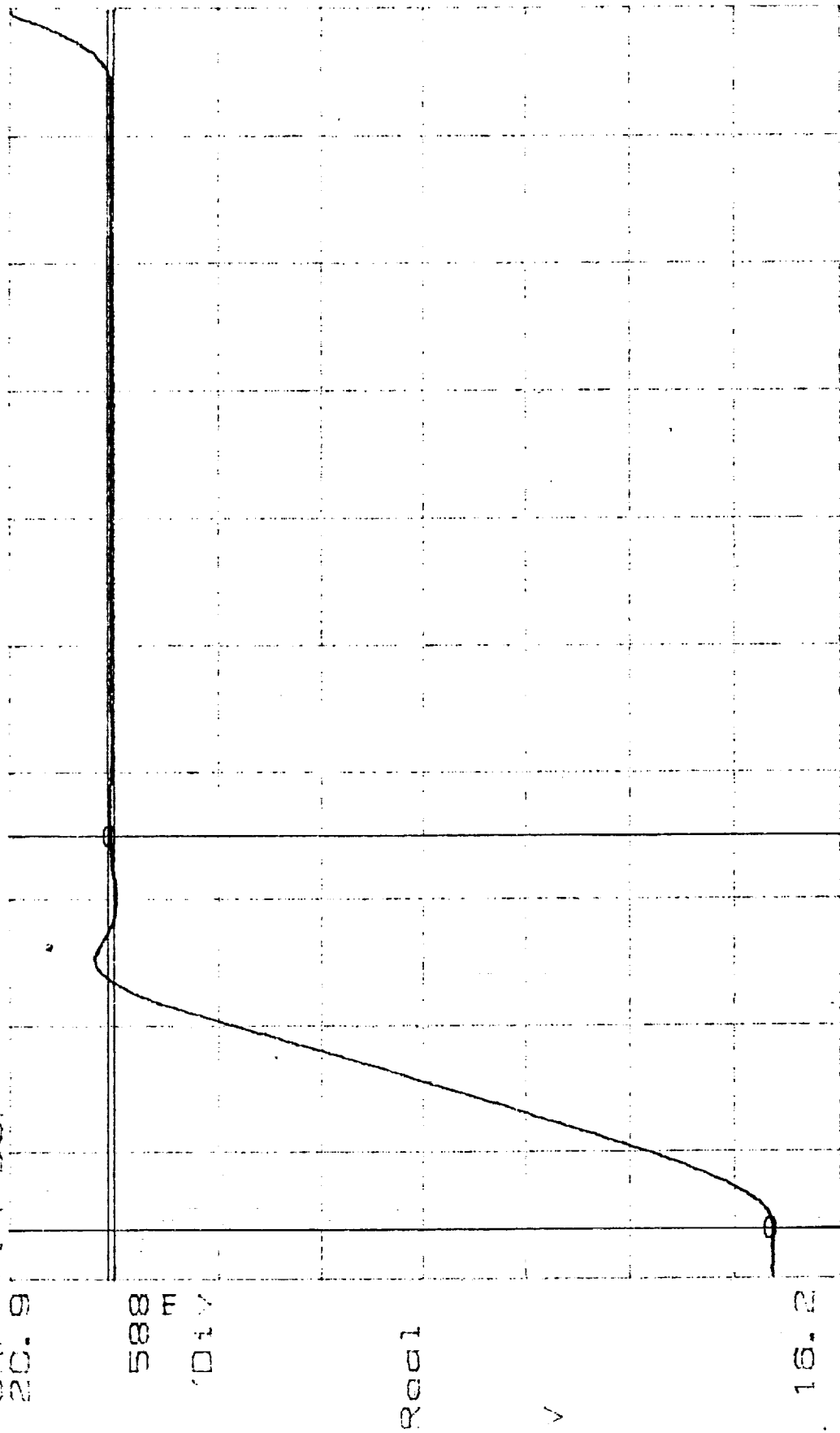
Test Eng: D. L. L. Date: 14/11/87

Quality: (7A) 269

B59

X=6.033 S     $\Delta X=210.2\text{ms}$     Y=20.3132     $\Delta Y=34.18\text{mV}$   
Y=16.5864     $\Delta Y=3.753\text{V}$

CAP TIM BUF  
20.9



16.2

ENDXY 6.01

Sec

7AP\_F55

6.68

S/N: 335168

P/N: 1331200-2-IT

S/N: 1108

Scan Motion and Jitter

TP 3.4.5.5

Step 30 - Cold Cal

Test Engr: D. Lee

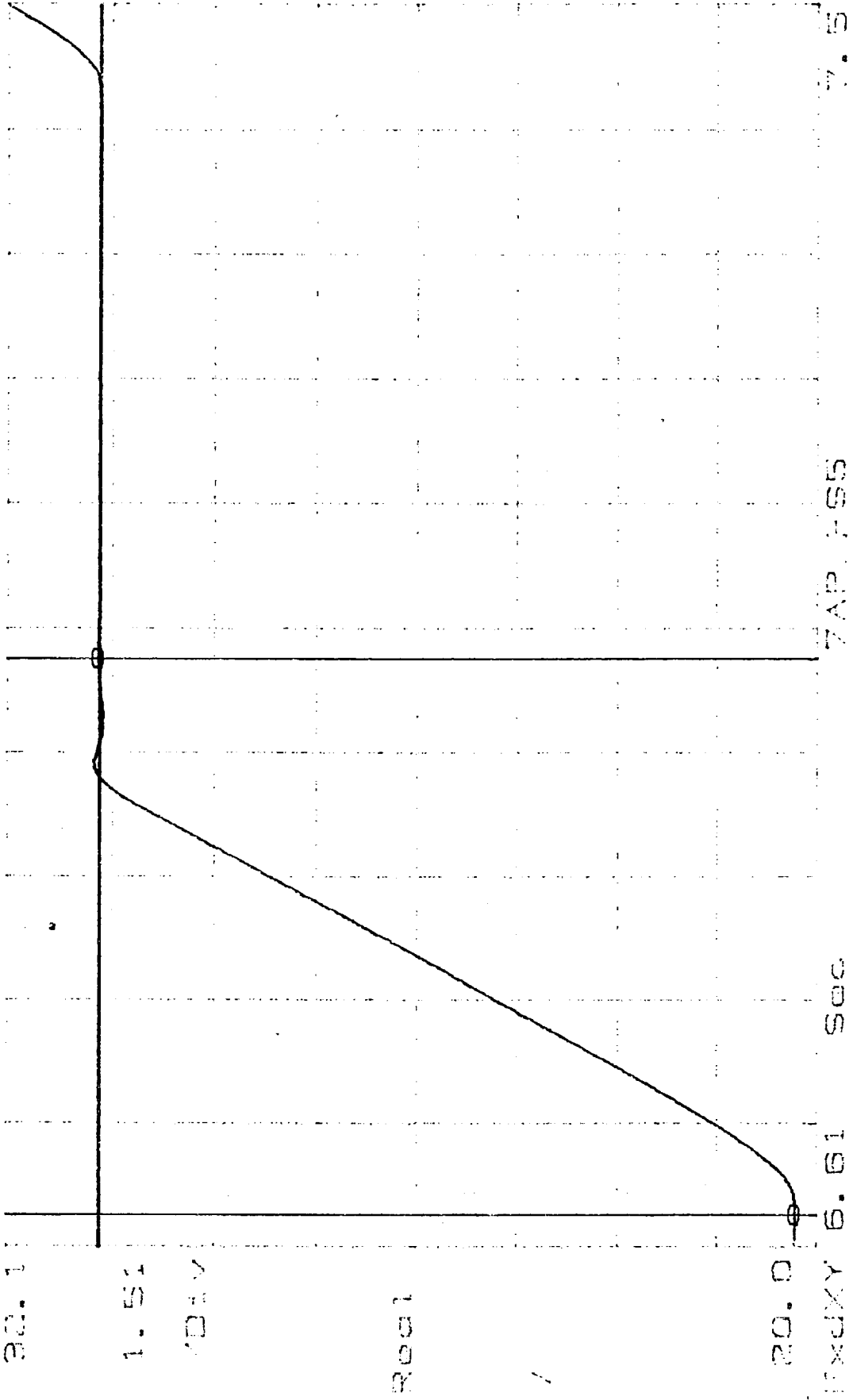
Quality: (298)

Date: 4/21/99

860

X=7.036 S    ΔX=400.0ms    Y=30.78    ΔY=36.67mV  
Y=30.7871    ΔY=10.45 V

CAP TIM 301  
30.1



S/O: 335168    Scan Motion and Titter    Test Engr: David    Date: 4/21/89  
P/N: 1331200-2-IT    P 3.4.5.5    Quality: (892)  
S/N: 108    Step Cold Cal - Warm Cal    B61



TEST DATA SHEET 7 (SHEET 1 OF 4)  
3.4.5.5: METSAT Scan Motion and Jitter Test

Test Setup Verified: *D. Lind*  
Signature

Shop Order No. 335168

Step No.	Description	Requirement	Test Result	Pass/Fail
7	--	Stepping Slewing <8 sec period per Figure 25	4.8 sec	P
9	Scene 1-2 3.33° step	<42 msec rise time per Figure 26	39.06 msec	P
		< ±5% jitter per Figure 26	± 0.691%	P
		< +4% overshoot for 19 msec	0.0%	P
10	Scene 2-3 3.33° step	<42 msec rise time per Figure 26	39.06 msec	P
		< ±5% jitter per Figure 26	± 1.025%	P
		< +4% overshoot for 19 msec	0.0%	P
11	Scene 3-4 3.33° step	<42 msec rise time per Figure 26	41.8 msec	P
		< ±5% jitter per Figure 26	± 0.873%	P
		< +4% overshoot for 19 msec	0.0%	P
12	Scene 4-5 3.33° step	<42 msec rise time per Figure 26	38.28 msec	P
		< ±5% jitter per Figure 26	± 1.654%	P
		< +4% overshoot for 19 msec	0.0%	P
13	Scene 5-6 3.33° step	<42 msec rise time per Figure 26	41.02 msec	P
		< ±5% jitter per Figure 26	± 1.48%	P
		< +4% overshoot for 19 msec	0.0%	P
14	Scene 6-7 3.33° step	<42 msec rise time per Figure 26	37.5 msec	P
		< ±5% jitter per Figure 26	± 2.09%	P
		< +4% overshoot for 19 msec	0.0%	P
15	Scene 7-8 3.33° step	<42 msec rise time per Figure 26	39.06 msec	P
		< ±5% jitter per Figure 26	± 1.01%	P
		< +4% overshoot for 19 msec	0.0%	P
16	Scene 8-9 3.33° step	<42 msec rise time per Figure 26	39.06 msec	P
		< ±5% jitter per Figure 26	± 1.45%	P
		< +4% overshoot for 19 msec	0.0%	P

Pass = P  
Fail = F

TEST DATA SHEET 7 (SHEET 2 OF 4)  
3.4.5.5: METSAT Scan Motion and Jitter Test

Step No.	Description	Requirement	Test Result	Pass/Fail
17	Scene 9-10 3.33° step	<42 msec rise time per Figure 26	40.23 mSec	P
		< ±5% jitter per Figure 26	± 1.61%	P
		< +4% overshoot for 19 msec	0.0%	P
18	Scene 10-11 3.33° step	<42 msec rise time per Figure 26	41.8 mSec	P
		< ±5% jitter per Figure 26	± 1.74%	P
		< +4% overshoot for 19 msec	0.0%	P
19	Scene 11-12 3.33° step	<42 msec rise time per Figure 26	41.41 mSec	P
		< ±5% jitter per Figure 26	± 1.38%	P
		< +4% overshoot for 19 msec	0.0%	P
20	Scene 12-13 3.33° step	<42 msec rise time per Figure 26	39.84 mSec	P
		< ±5% jitter per Figure 26	± 1.04%	P
		< +4% overshoot for 19 msec	0.0%	P
21	Scene 13-14 3.33° step	<42 msec rise time per Figure 26	41.8 mSec	P
		< ±5% jitter per Figure 26	± 1.06%	P
		< +4% overshoot for 19 msec	0.0%	P
22	Scene 14-15 3.33° step	<42 msec rise time per Figure 26	39.06 mSec	P
		< ±5% jitter per Figure 26	± 1.99%	P
		< +4% overshoot for 19 msec	0.0%	P
23	Scene 15-16 3.33° step	<42 msec rise time per Figure 26	41.02 mSec	P
		< ±5% jitter per Figure 26	± 1.37%	P
		< +4% overshoot for 19 msec	0.0%	P
24	Scene 16-17 3.33° step	<42 msec rise time per Figure 26	41.41 mSec	P
		< ±5% jitter per Figure 26	± 1.45%	P
		< +4% overshoot for 19 msec	0.0%	P

Pass = P  
Fail = F

**TEST DATA SHEET 7 (SHEET 3 OF 4)**  
**3.4.5.5: METSAT Scan Motion and Jitter Test**

Step No.	Description	Requirement	Test Result	Pass/Fail
25	Scene 17-18 3.33° step	<42 msec rise time per Figure 26	41.4/mSec	P
		< ±5% jitter per Figure 26	± 1.55%	P
		< +4% overshoot for 19 msec	0.0%	P
26	Scene 18-19 3.33° step	<42 msec rise time per Figure 26	41.8 mSec	P
		< ±5% jitter per Figure 26	± 0.893%	P
		< +4% overshoot for 19 msec	0.0%	P
27	Scene 19-20 3.33° step	<42 msec rise time per Figure 26	40.62 mSec	P
		< ±5% jitter per Figure 26	± 1.156%	P
		< +4% overshoot for 19 msec	0.0%	P
28	Scene 20-21 3.33° step	<42 msec rise time per Figure 26	41.02 mSec	P
		< ±5% jitter per Figure 26	± 1.39%	P
		< +4% overshoot for 19 msec	0.0%	P
29	Scene 21-22 3.33° step	<42 msec rise time per Figure 26	41.8 mSec	P
		< ±5% jitter per Figure 26	± 1.58%	P
		< +4% overshoot for 19 msec	0.0%	P
30	Scene 22-23 3.33° step	<42 msec rise time per Figure 26	41.4/mSec	P
		< ±5% jitter per Figure 26	± 1.68%	P
		< +4% overshoot for 19 msec	0.0%	P
31	Scene 23-24 3.33° step	<42 msec rise time per Figure 26	39.84 mSec	P
		< ±5% jitter per Figure 26	± 1.98%	P
		< +4% overshoot for 19 msec	0.0%	P
32	Scene 24-25 3.33° step	<42 msec rise time per Figure 26	41.4/mSec	P
		< ±5% jitter per Figure 26	± 1.11%	P
		< +4% overshoot for 19 msec	0.0%	P

Pass = P  
Fail = F

TEST DATA SHEET 7 (SHEET 4 OF 4)  
3.4.5.5: METSAT Scan Motion and Jitter Test

Step No.	Description	Requirement	Test Result	Pass/Fail
33	Scene 25-26 3.33° step	<42 msec rise time per Figure 26	41.8 msec	P
		< ±5% jitter per Figure 26	± 1.92%	P
		< +4% overshoot for 19 msec	0.0%	P
34	Scene 26-27 3.33° step	<42 msec rise time per Figure 26	40.23 msec	P
		< ±5% jitter per Figure 26	± 1.88%	P
		< +4% overshoot for 19 msec	0.0%	P
35	Scene 27-28 3.33° step	<42 msec rise time per Figure 26	41.8 msec	P
		< ±5% jitter per Figure 26	± 1.47%	P
		< +4% overshoot for 19 msec	0.0%	P
36	Scene 28-29 3.33° step	<42 msec rise time per Figure 26	41.41 msec	P
		< ±5% jitter per Figure 26	± 1.68%	P
		< +4% overshoot for 19 msec	0.0%	P
37	Scene 29-30 3.33° step	<42 msec rise time per Figure 26	40.23 msec	P
		< ±5% jitter per Figure 26	± 1.80%	P
		< +4% overshoot for 19 msec	0.0%	P
38	Scene 30- Cold Cal 35.0° slew	<0.21 sec slew time per Figure 29	< 0.21 sec	P
		< ±5% jitter per Figure 30	< ± 5%	P
39	Cold Cal - Warm Cal 96.67° slew	<0.40 sec slew time per Figure 31	< 0.40 sec	P
		< ±5% jitter per Figure 32	< ± 5%	P

Pass = P  
Fail = F

Unit: 1331200-2-IT

Test Engineer: D. Lind

Serial No.: 108

Quality Assurance: 892 74

Date: 4/21/99

Customer Representative: G. Salacga 5/5/99

X=6.1691 Sec  
Y=39.7694mV

CAP TIM BUF

70.0 F

10.0 F

/DIV

500 mA/  
10mV

Y=40.0363m ΔY=2.036mV

$$I_{PK} = 39.769mV \left( \frac{500mA}{10mV} \right) = \underline{\underline{1.988mA}}$$

Real

V

-10

mV

FXDXY 0.0

Sec

4PLB\_C

8.0

S/O: 335168

P/N: 1331200-2-IT

S/N: 1108

28V PLB Peak Current

File: 4PLB\_C

R 3.4.5.6

Test Engr: Dated Date: 4/23/69

Quality: (89%)

C1

TEST DATA SHEET 8  
3.4.5.6: METSAT Pulse Load Bus Current

Test Setup Verified: D. Lush  
Signature

Shop Order No. 335168

3.4.5.6: 28V Bus Peak Current and Rise Time Test

Step No.	Requirement	Test Result	Pass/Fail
4	< 2 A peak any place in the scan	1.988 mA	P
5	> 70 $\mu$ sec rise time, 3.33° step	2.34 mSec	P
6	> 70 $\mu$ sec rise time, start of WC slew	2.43 mSec	P
6	> 70 $\mu$ sec rise time, end of WC slew	2.34 mSec	P

Pass = P  
Fail = F

Unit: 1331200-2-IT.

Test Engineer: D. Lush

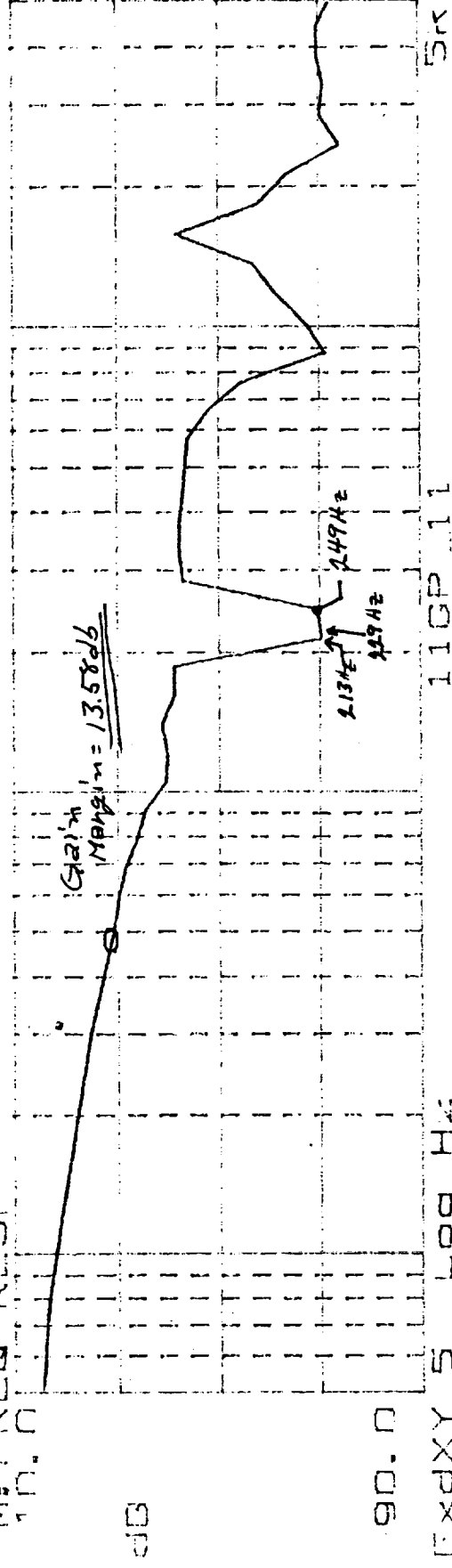
Serial No.: 108

Quality Assurance: (892) JL

Date: 4/23/99

X=48.025 Hz  
Y=-13.582 dB

M: FREQ RESP  
11.0



FxdXY 5 Log Hz  
Yb=-180.65 Deg  
M: FREQ RESP  
90.0

Phase  
Deg

-720  
FxdXY 5 Log Hz

11GP\_11

SN: 335168

PN: 1331200-2-IT

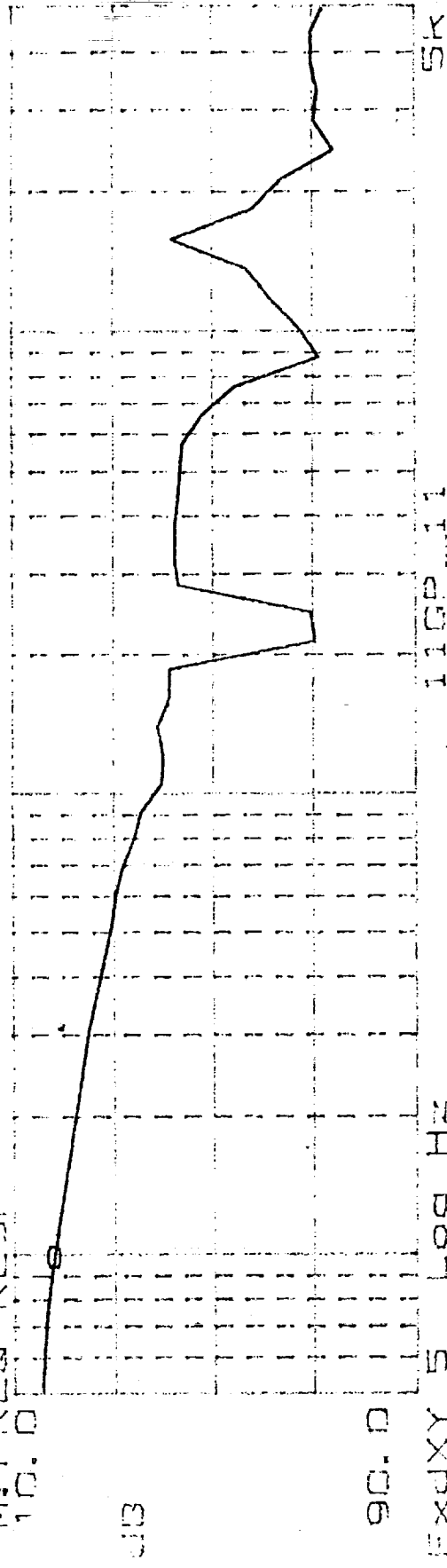
SN: 108

Gain Phase Margin Test Test Engr: Q. L. L. Date: 4/19/99

PF 3.45.8 Quality: 89%

File: 11GP\_11

X=9.8905 HZ  
 Y=11.383mDB  
 M:FREQ RESP  
 10.0



FxdXY 5 Log HZ  
 Yb=-15.5 Deg  
 M:FREQ RESP  
 90.0

Phase Margin =  $180 - 115.5 = 64.5^\circ$

Phase

Deg

-720

FxdXY 5 Log HZ

11GP 11

5K

S/N: 335168

P/N: 1331200-2-IT

S/N: 108

Gain Phase Margin Test Test Engr: D. Ford Date: 4/19/99  
 HP 3.45.8 Quality: 80%

File: 11GP-11

D2

X=47.612 Hz  
Y=-13.582 dB

M: FREQ RESP  
D.N

JD

Gain Margin = 13.58dB

90.0

FxdXY 5 Log Hz

Yb=-180.15 Deg

M: FREQ RESP

90.0

12GP\_21

5k

Phase

Deg

-720

FxdXY 5 Log Hz

S/O: 335/68

PN: 1331200-2-IT

SN: 108

12GP\_21

5k

Gain Phase Margin Test

IP 3.4.5.8

File: 12GP\_21

Test Engr: D. Lund Date: 4/9/99

Quality: 88%

D3

X=9.8905 Hz

Y=-4.1697 mdB

M: FREQ RESP

10.0

dB

-90.0

FxdXY 5 Log Hz

Yb=-115.85 Deg

M: FREQ RESP

90.0

Phase

Deg

-720

FxdXY 5 Log Hz

90: 335168

PN: 1331200-2-IT

SN: 108

Gain Phase Margin Test

PM 34.5.8

File: 12GP\_21

Test Engn: D. J. L. L. Date: 4/9/99

Quality: 2.8

D4

X=48.025 HZ  
Y=-13.643 dB

M: FREQ RESP  
10.0

dB

Gain Margin = 13.64dB

90.0

FxdXY 5 Log HZ

Yb=-181.0 Deg

M: FREQ RESP

90.0

Phase

Deg

-720

FxdXY 5 Log HZ

S/O: 335/68

P/N: 1331200-2-IT

S/N: 108

Gain Phase Margin Test

PF 3.45.8

File: 12GP\_31

Test Engineer: DeLund Date: 4/19/99

Quality: 288

D5

X=9.8905 Hz

Y=11.6627 mdB

F-M: FREQ RESP

10.0

dB

-90.0

FxdXY 5 Log Hz

Yb=-115.81 Deg

F-M: FREQ RESP

90.0

12GP\_31

5K

Phase Margin =  $180 - 115.81 = 64.19^\circ$

Phase

Deg

-720

FxdXY 5 Log Hz

12GP\_31

5K

S/O: 335168

P/N: 1331200-2-JT

S/N: 108

Gain Phase Margin Test

PM 3.4, 5.8

File: 12GP\_31

Test Eng: D. L. L. Date: 4/19/99

Quality: 2.7

D6

TEST DATA SHEET 9  
3.4.5.8: METSAT Gain/Phase Margin Test

Test Setup Verified: D. Lund  
Signature

Shop Order No. 335768

3.4.5.8 Step 12: Gain/Phase Margin Test

Requirement	Test Result		Pass/Fail
12 dB minimum	1	13.58	P
	2	13.58	
	3	13.64	
25 degrees minimum	1	64.50	P
	2	64.15	
	3	64.19	

Pass = P  
Fail = F

Unit: 1331200-2

Serial No.: 108

Date: 4/19/99

Test Engineer: D. Lund

Quality Assurance: (7A 268)

Customer Representative: P. Palacgan  
5/5/99



X=78.12 Hz dBVrms

Y=10.0

ΔY=80.0

POWER SPECTRUM

3AVG 0.00010 0.010

10.0

10.0

0.01V

$$\text{Gain Margin (dB)} = 20 \log \frac{1 + \frac{R_{S8} + R_{p11}}{2K}}{1 + \frac{R_{S8}}{2K}} = \frac{1 + \frac{18.266K + 40.315K}{2K}}{1 + \frac{18.266K}{2K}} = 9.51dB$$

dB

PSD  
V2

-70.0

EXCXY 0 Hz

0.001 PSI

312

S/N: 335/68

P/N: 1331200-2-IT

S/N: 108

Operational Gain Margin Test

F 3.45.9

Run #1

Test Engr: 10/10/97 Date: 4/24/97  
Quality: (80/90)

E1

**TEST DATA SHEET 10**  
**3.4.5.9: METSAT Operational Gain Margin Test**

Test Setup Verified: D. L. L.  
Signature

Shop Order No. 335168

3.4.5.9: Operation Gain Margin Test

Step No.	Requirement		Test Result	Pass/Fail
11	R58 Resistance (Kohms)		18.266 K	} P
	Test Pot Resistance (Kohms)	1	40.315 K	
		2	40.877 K	
12	Oscillation Frequency (Hz)	3	39.805 K	} P
		1	78.12 Hz	
		2	77.73 Hz	
16	Gain Margin, 9 dB minimum	3	77.73 Hz	} P
		1	9.51 dB	
		2	9.59 dB	
		3	9.44 dB	

Pass = P  
Fail = F


Unit: 1331200-2-IT

Test Engineer: D. L. L.

Serial No.: 108

Quality Assurance: 17

Date: 4/22/99

 <b>NASA</b> National Aeronautics and Space Administration		Report Documentation Page	
1. Report No. ---	2. Government Accession No. ---	3. Recipient's Catalog No. ---	
4. Title and Subtitle  Integrated Advanced Microwave Sounding Unit-A (AMSU-A), Performance Verification Report		5. Report Date 4 August 1999	
		6. Performing Organization Code ---	
7. Author(s)  L. Paliwoda		8. Performing Organization Report No. 11486	
		10. Work Unit No. ---	
9. Performing Organization Name and Address Aerojet 1100 W. Hollyvale Azusa, CA 91702		11. Contract or Grant No. NAS 5-32314	
		13. Type of Report and Period Covered Final	
12. Sponsoring Agency Name and Address NASA Goddard Space Flight Center Greenbelt, Maryland 20771		14. Sponsoring Agency Code ---	
15. Supplementary Notes  ---			
16. ABSTRACT (Maximum 200 words )  This is the Performance Verification Report, Antenna Drive Subassembly, Antenna Drive Subsystem, METSAT AMSU-A2 (P/N 1331200-2, SN: 108), for the Integrated Advanced Microwave Sounding Unit-A (AMSU-A).			
17. Key Words (Suggested by Author(s))  EOS Microwave System		18. Distribution Statement  Unclassified --- Unlimited	
19. Security Classif. (of this report)  Unclassified	20. Security Classif. (of this page)  Unclassified	21. No. of pages	22. Price  ---

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1. AGENCY USE ONLY (Leave blank)		2. REPORT DATE		3. REPORT TYPE AND DATES COVERED
4. TITLE AND SUBTITLE <b>Integrated Advanced Microwave Sounding Unit-A (AMSU-A), Performance Verification Report</b>			5. FUNDING NUMBERS  <b>NAS 5-32314</b>	
6. AUTHOR(S) <b>C. Haapala</b>				
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) <b>Aerojet 1100 W. Hollyvale Azusa, CA 91702</b>			8. PERFORMING ORGANIZATION REPORT NUMBER  <b>11486 4 August 1999</b>	
9. SPONSORING/MONITORING AGENCY NAME(S) AND ADDRESS(ES) <b>NASA Goddard Space Flight Center Greenbelt, Maryland 20771</b>			10. SPONSORING/MONITORING AGENCY REPORT NUMBER  <b>---</b>	
11. SUPPLEMENTARY NOTES  <b>---</b>				
12a. DISTRIBUTION/AVAILABILITY STATEMENT  <b>---</b>			12b. DISTRIBUTION CODE  <b>---</b>	
13. ABSTRACT (Maximum 200 words)  <b>This is the Performance Verification Report, Antenna Drive Subassembly, Antenna Drive Subsystem, METSAT AMSU-A2 (P/N 1331200-2, SN: 108), for the Integrated Advanced Microwave Sounding Unit-A (AMSU-A).</b>				
14. SUBJECT TERMS  <b>EOS Microwave System</b>			15. NUMBER OF PAGES  <b>---</b>	
17. SECURITY CLASSIFICATION OF REPORT <b>Unclassified</b>			18. SECURITY CLASSIFICATION OF THIS PAGE <b>Unclassified</b>	
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